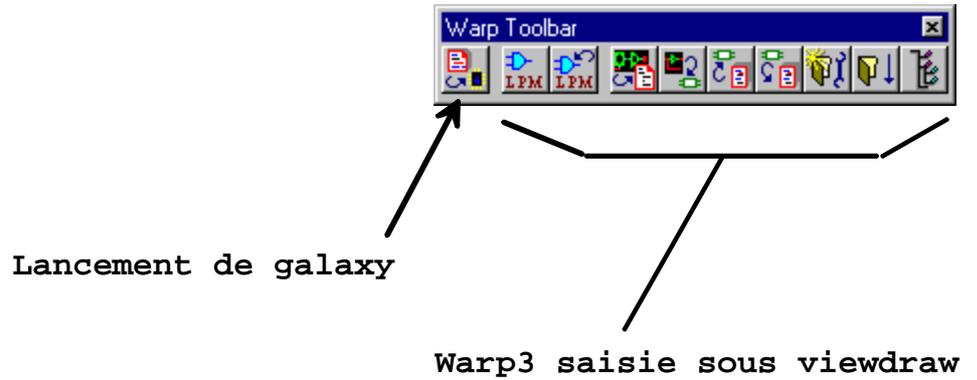
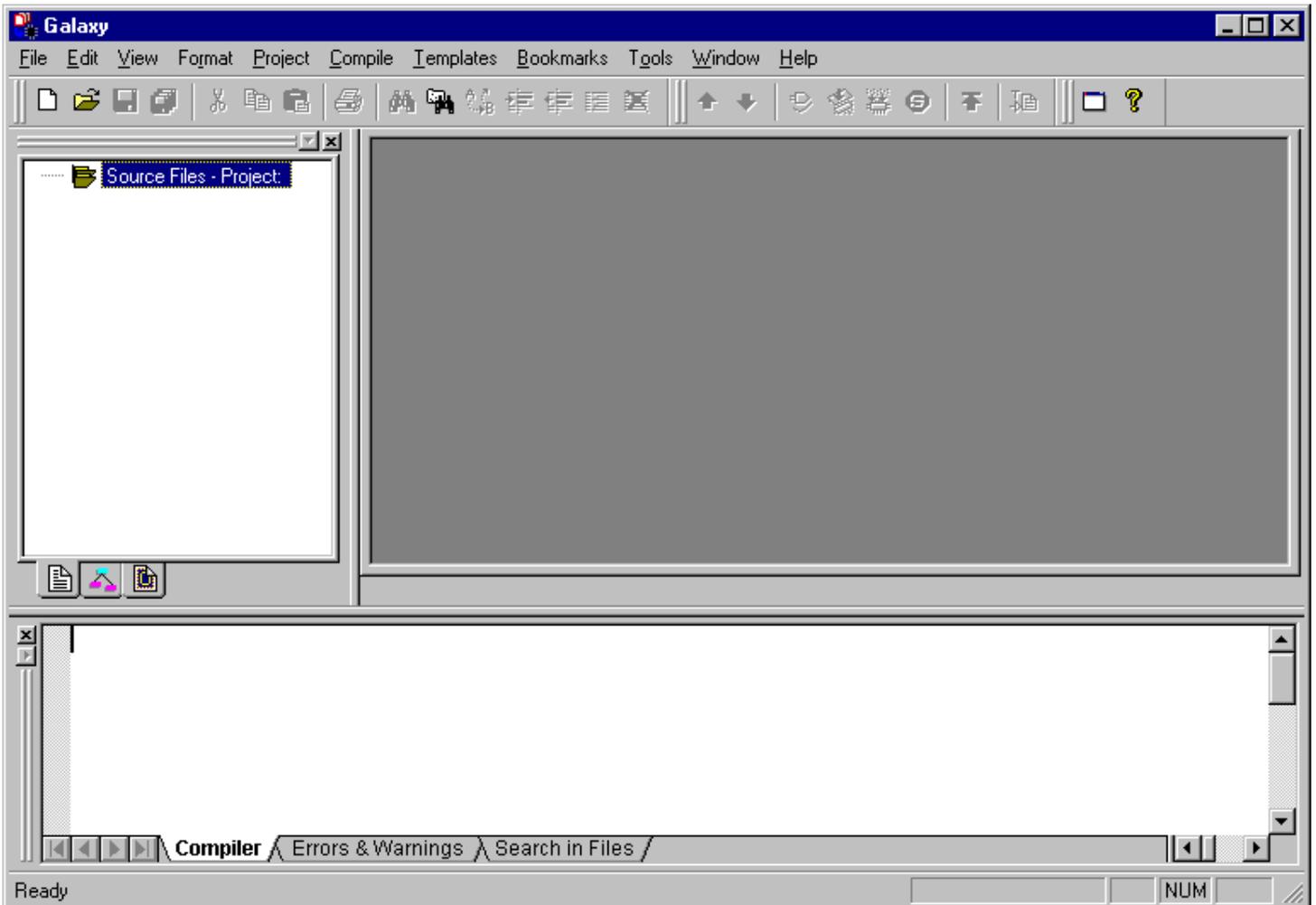


SYNTHESE VHDL AVEC GALAXY

LA BARRE D'OUTILS DE GALAXY



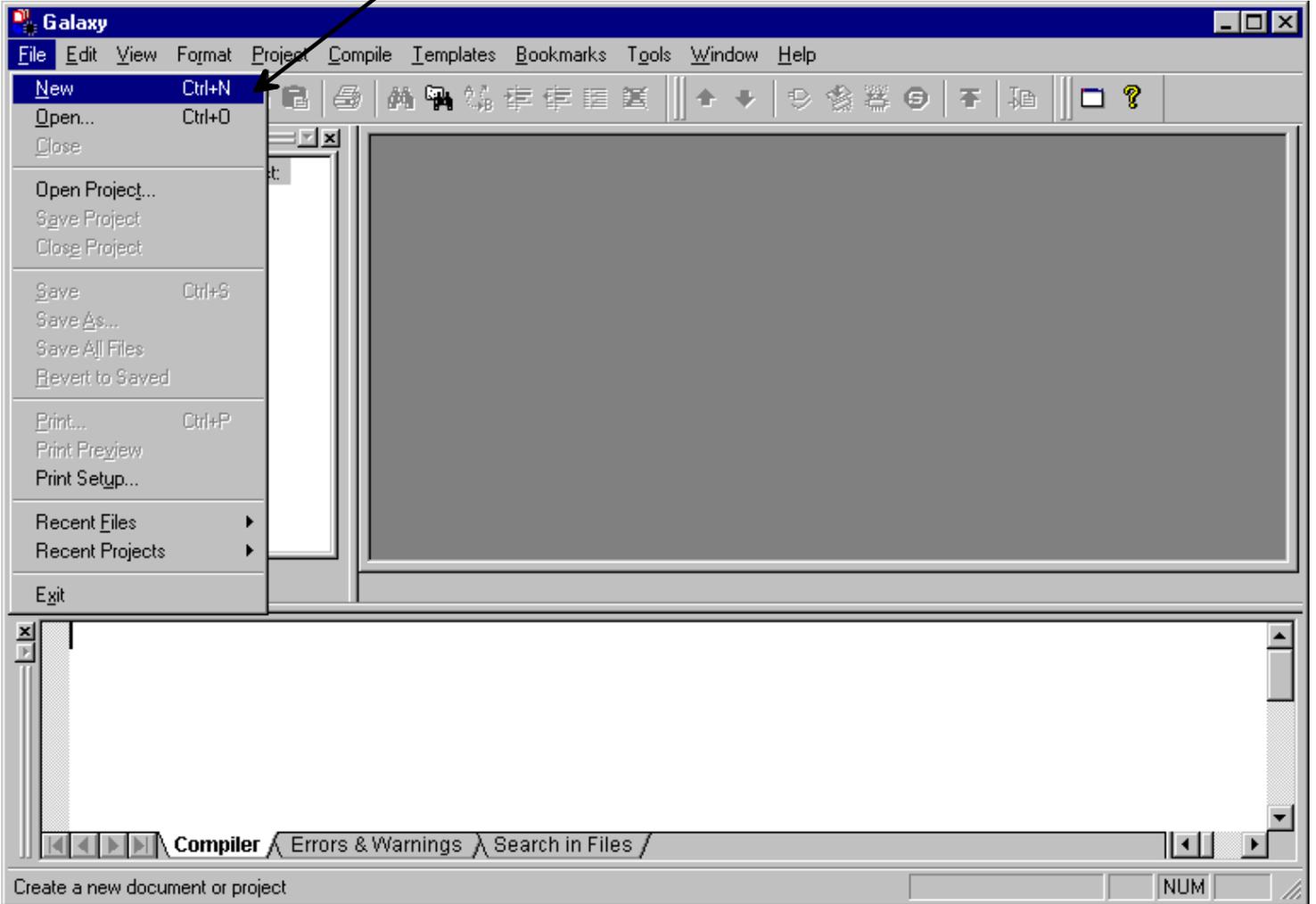
DEMARRAGE DE GALAXY



SYNTHESE VHDL AVEC GALAXY

RECUPERATION D'UN FICHIER EXISTANT

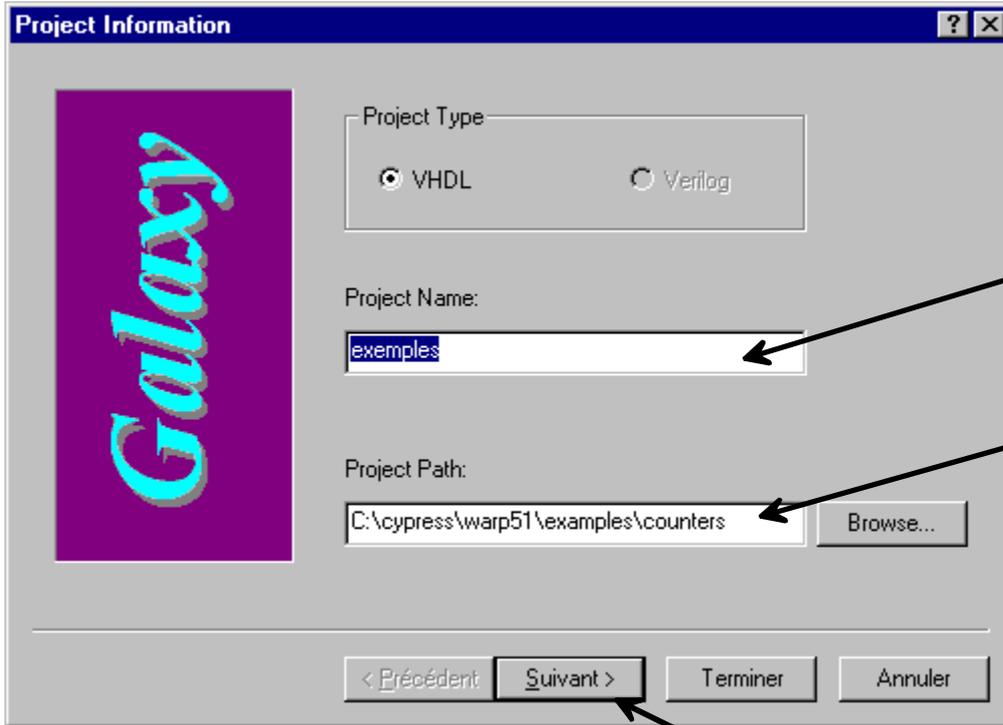
Faire Fichier Nouveau



Choisir Nouveau projet

SYNTHESE VHDL AVEC GALAXY

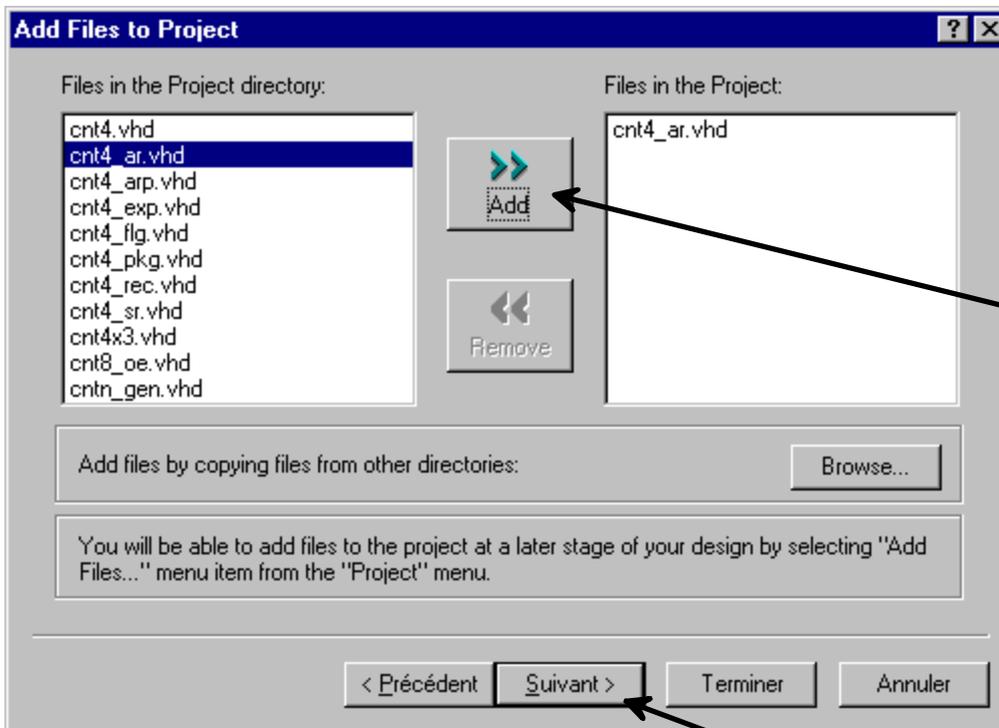
RECUPERATION DE FICHIERS EXISTANTS



Donner le nom
du projet

Donner le répertoire
du projet

Pour continuer

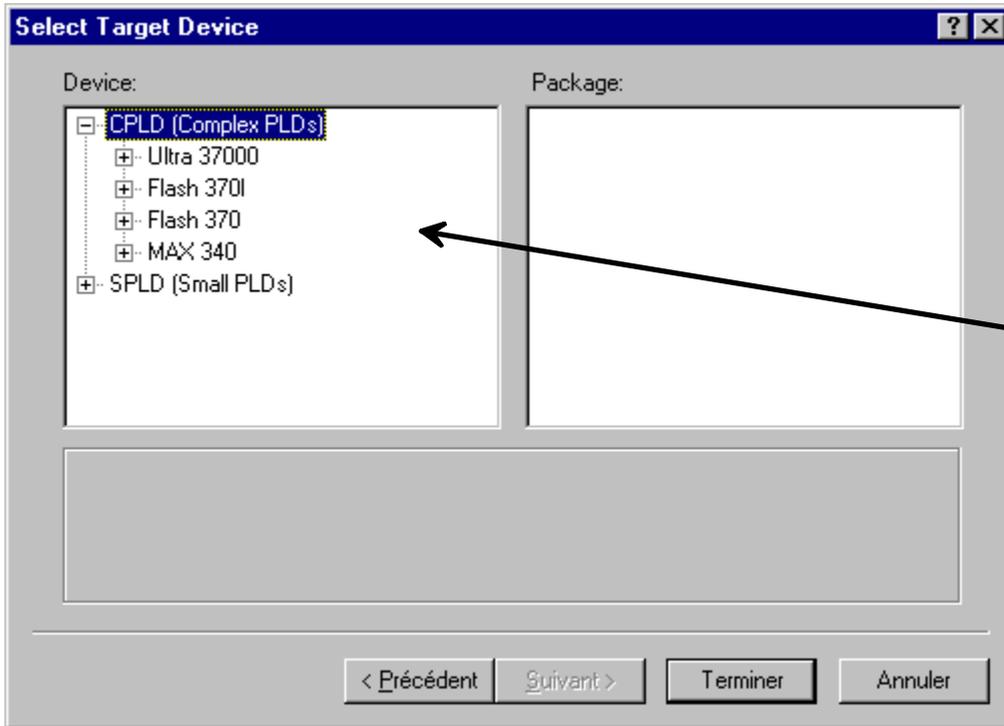


Ajout des fichiers
vhdl faisant partie
du projet

Pour continuer

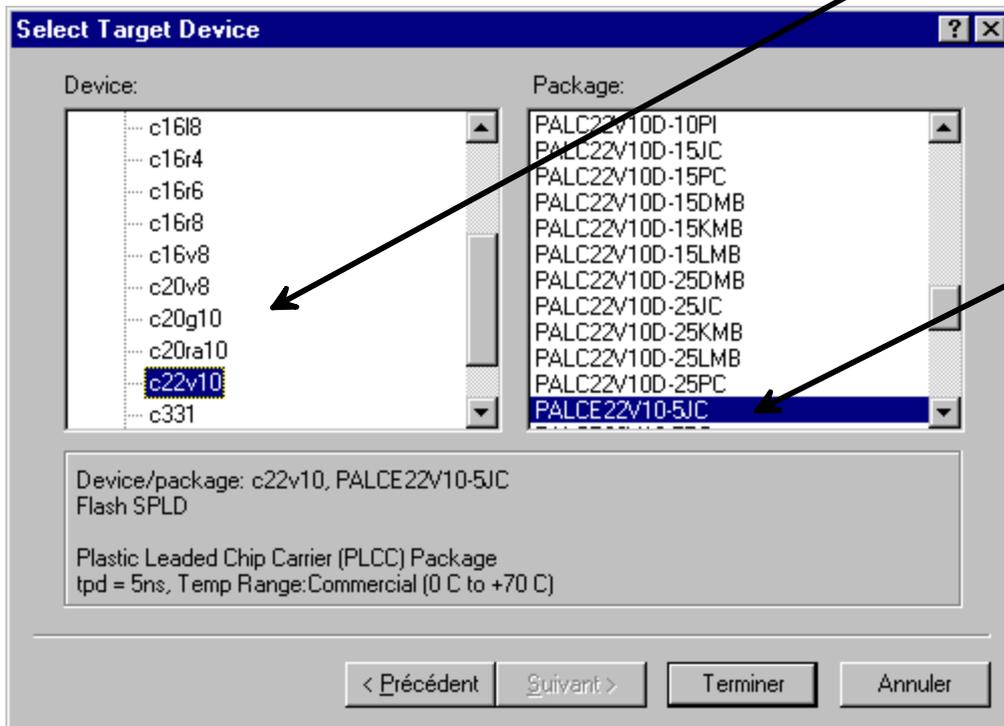
SYNTHESE VHDL AVEC GALAXY

CHOIX DU COMPOSANT CIBLE (DEVICE)



Choix de la famille

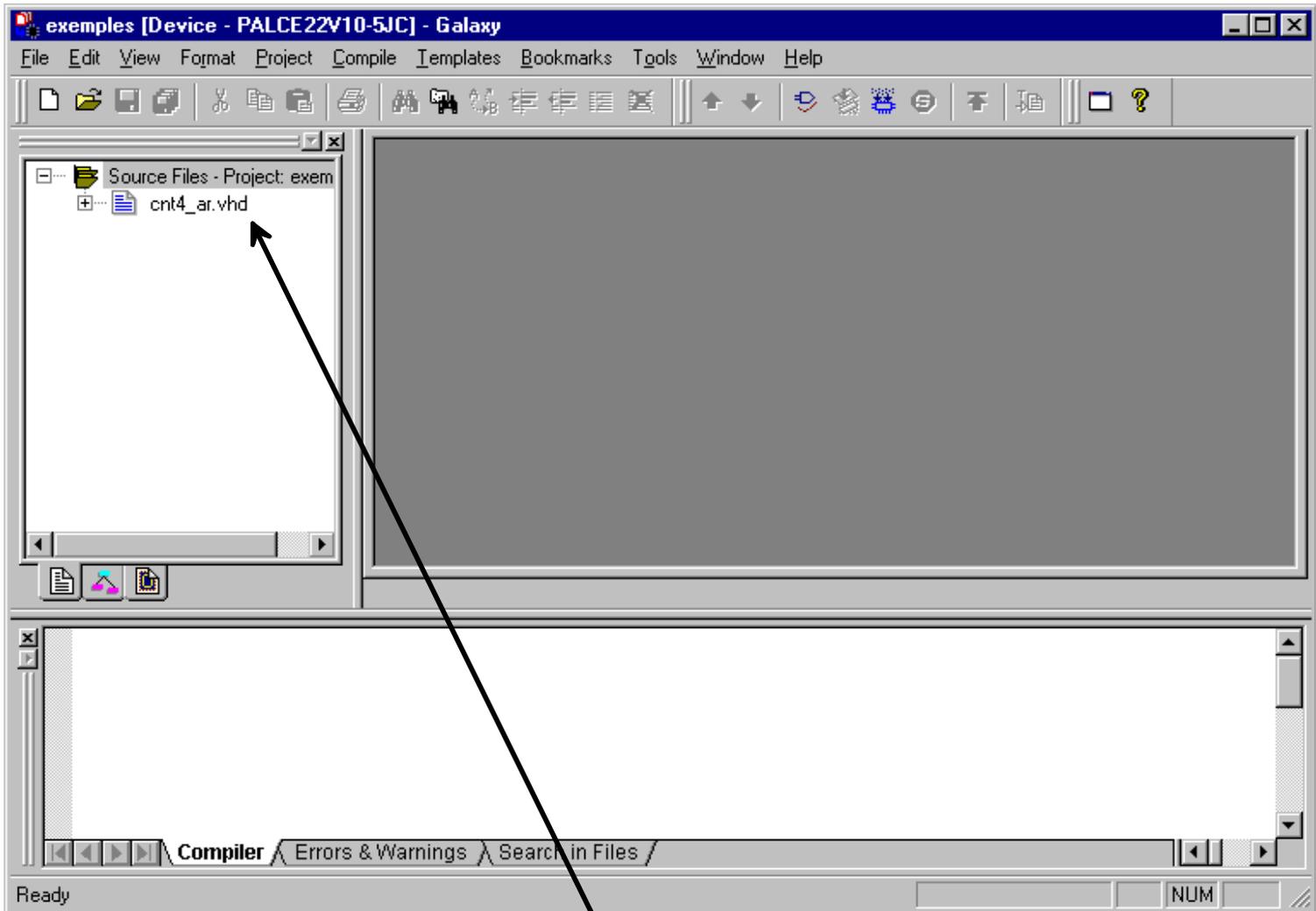
Choix du composant



Choix du boîtier,
choix du tpd ect ..

SYNTHESE VHDL AVEC GALAXY

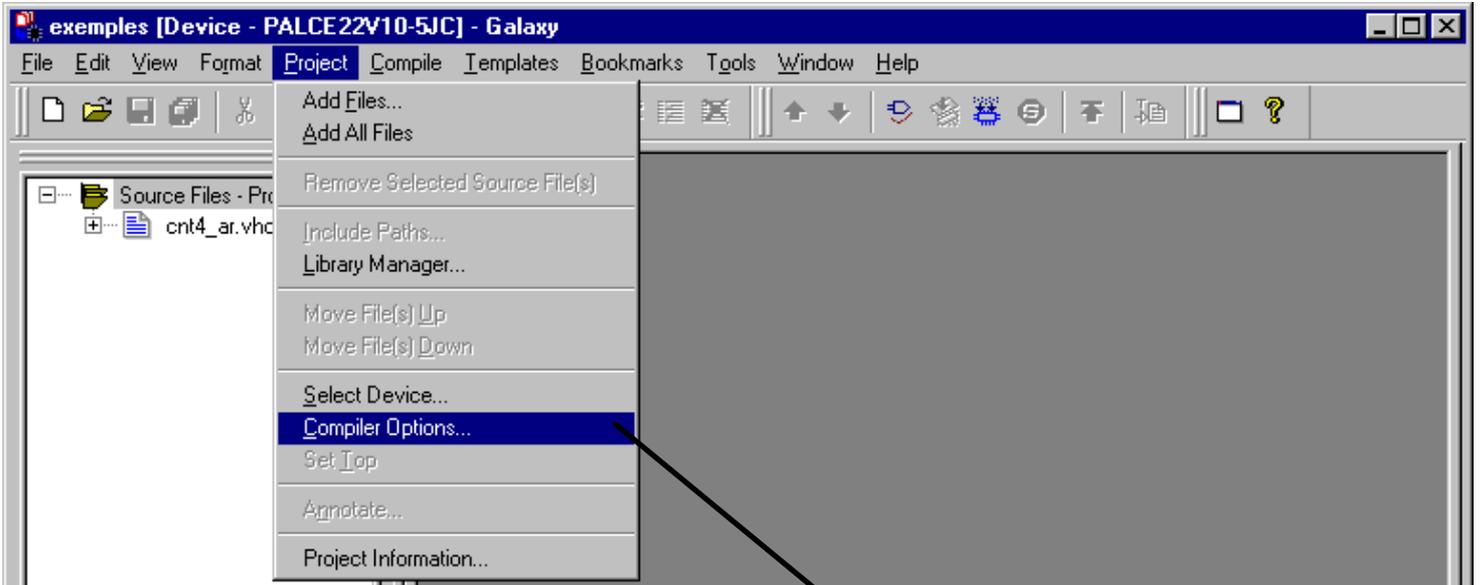
OUVERTURE DE GALAXY



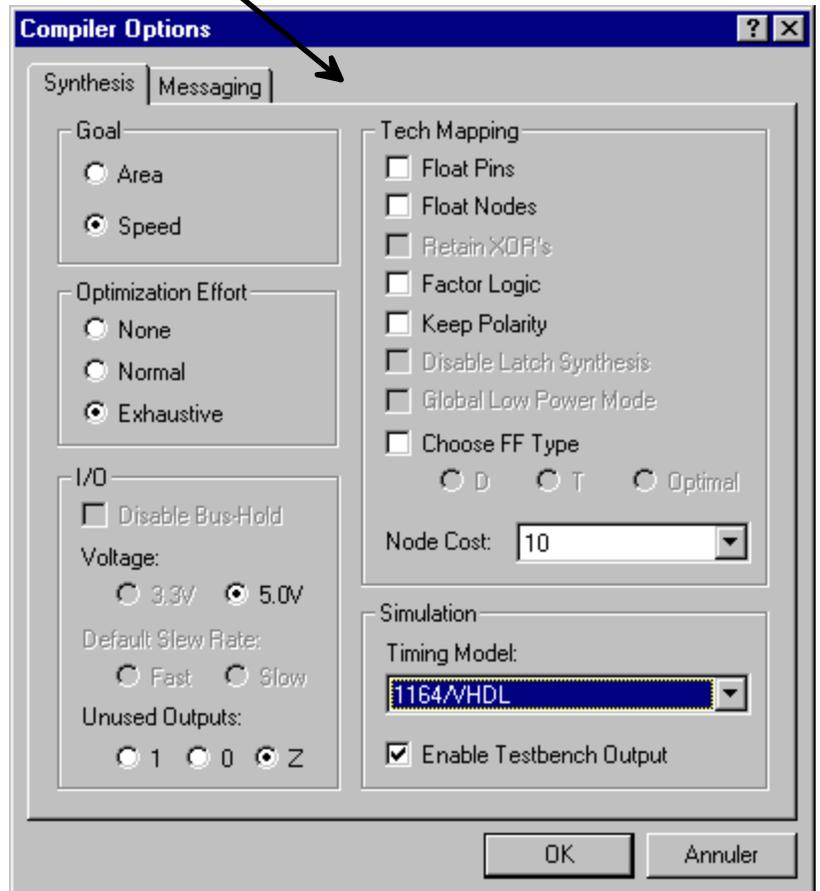
Listes des fichiers
vhdl présent dans le projet

SYNTHESE VHDL AVEC GALAXY

PREPARATION A LA COMPILATION

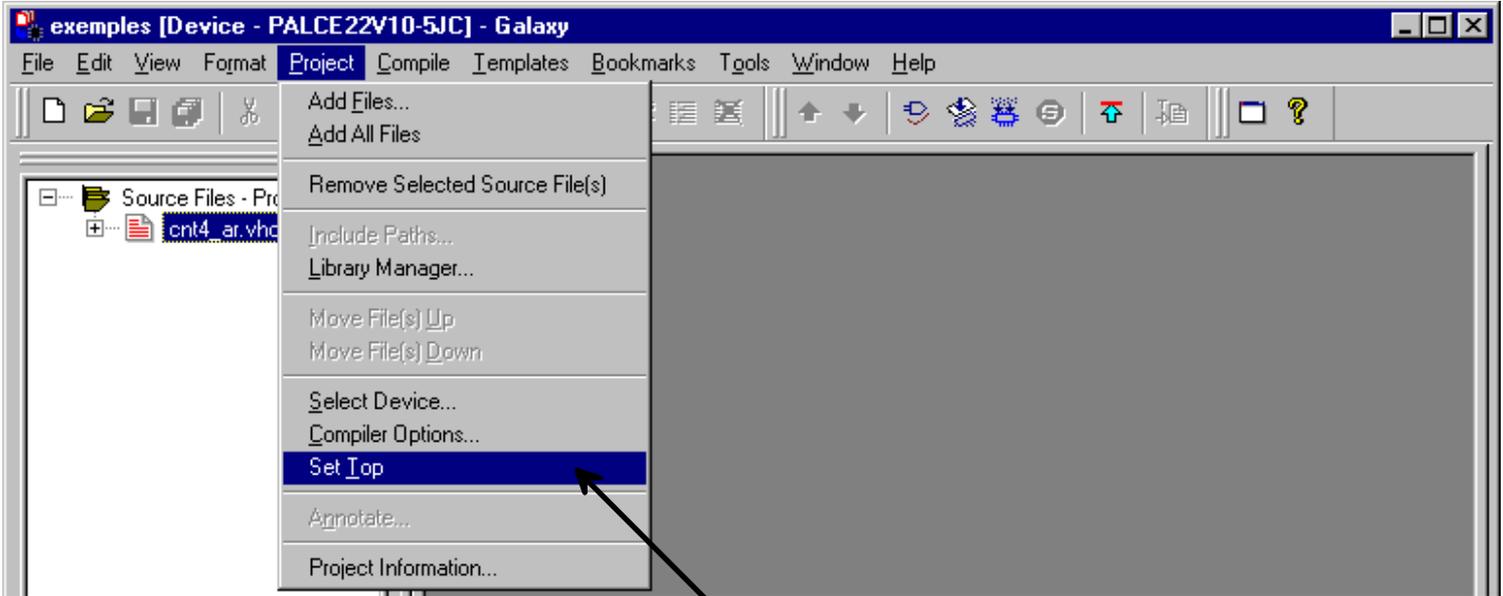


Saisie des options de compilation



SYNTHESE VHDL AVEC GALAXY

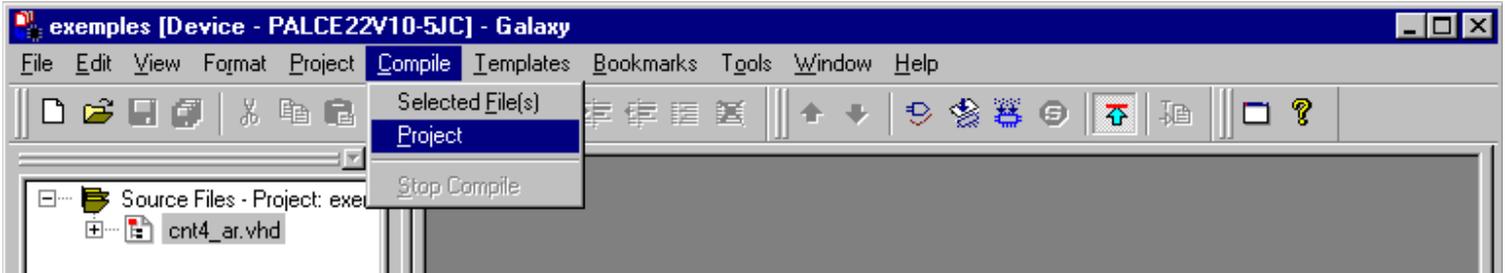
DESIGNATION DE L'ORIGINE DE LA DESCRIPTION VHDL OPTION SET TOP



Désignation du fichier
contenant le point de
départ de la description vhd1

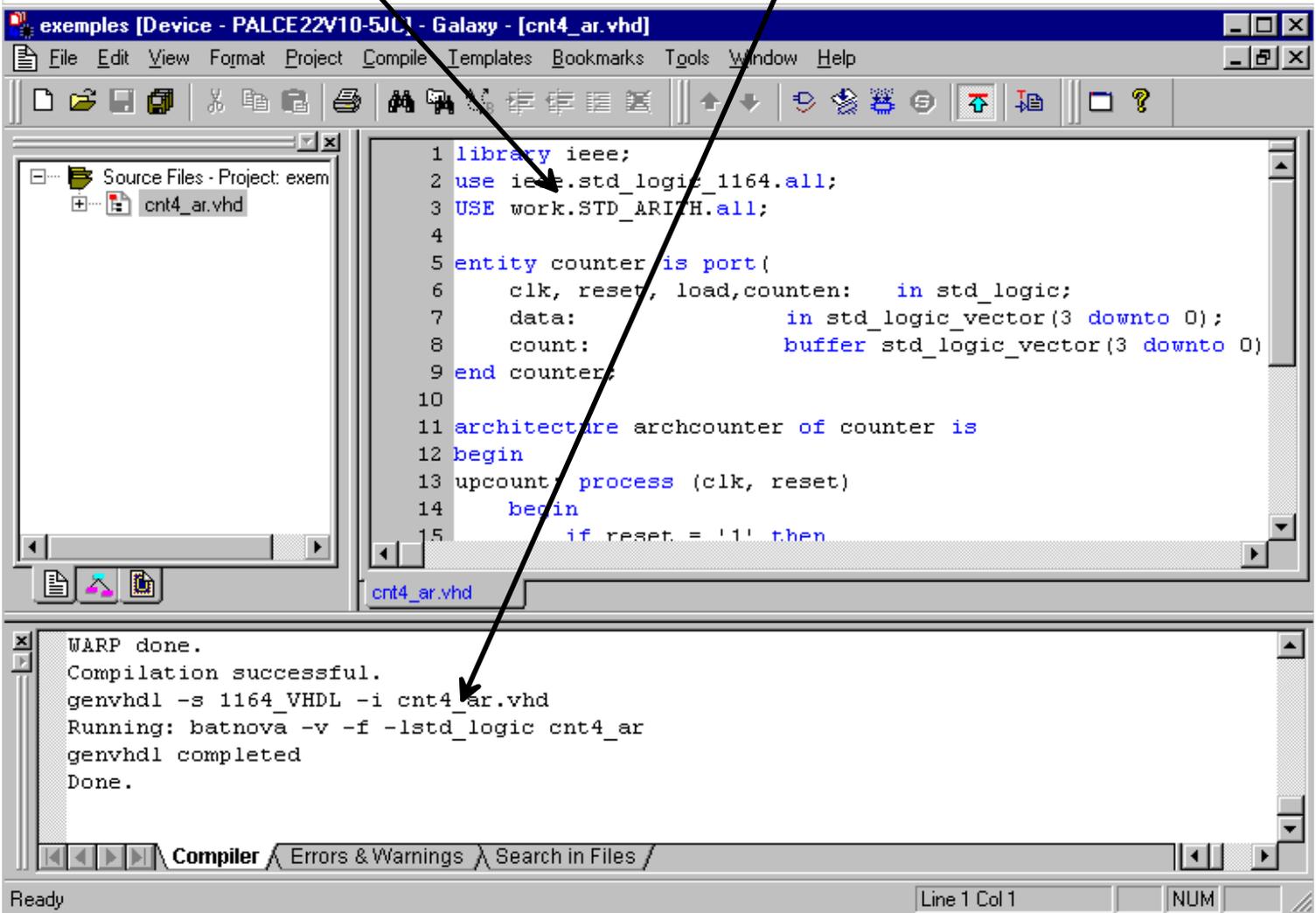
SYNTHESE VHDL AVEC GALAXY

COMPILATION DU PROJET



Texte vhd1

Résultat de la compilation

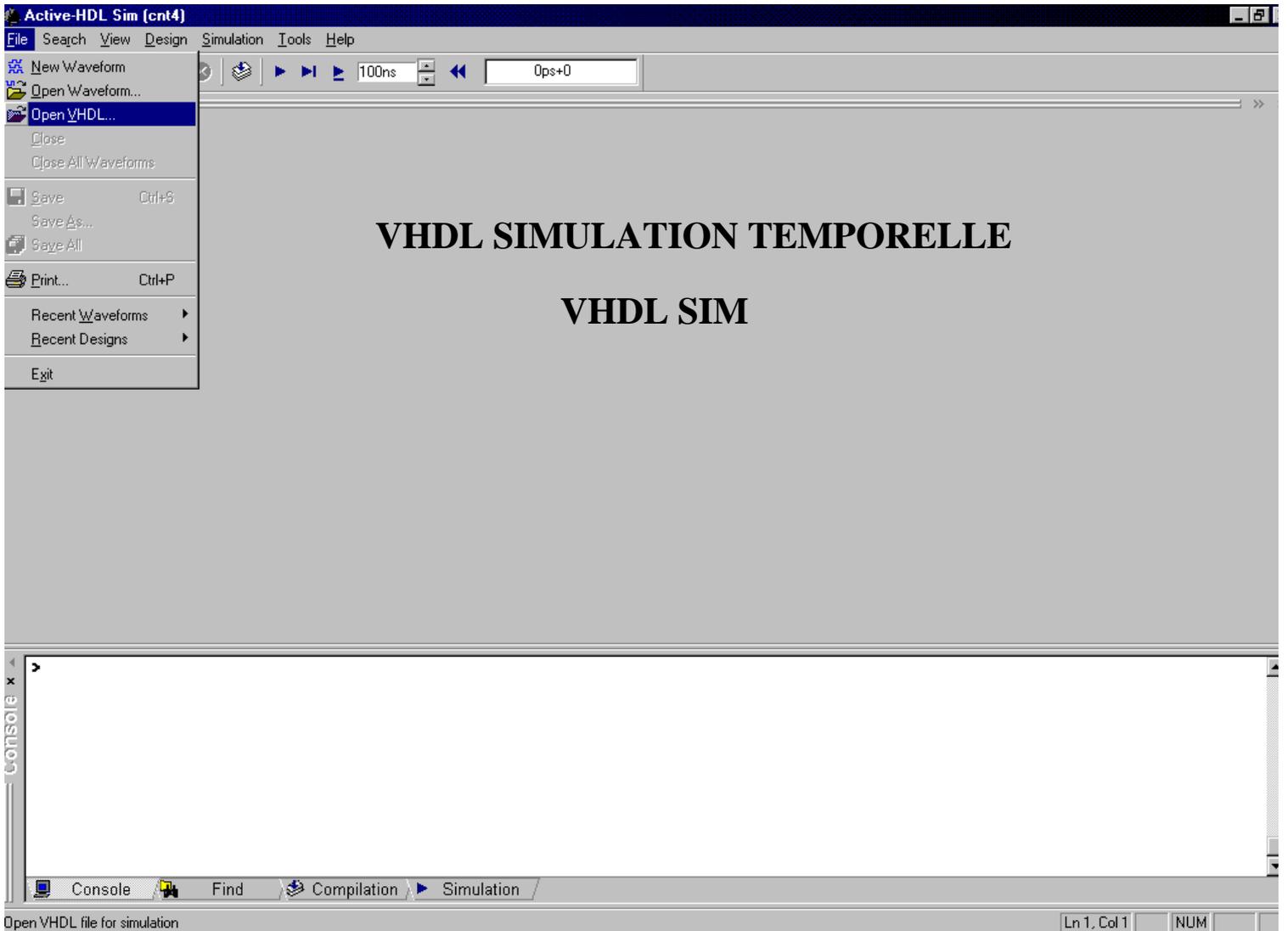


SYNTHESE VHDL AVEC GALAXY

SIMULATION DU PROJET

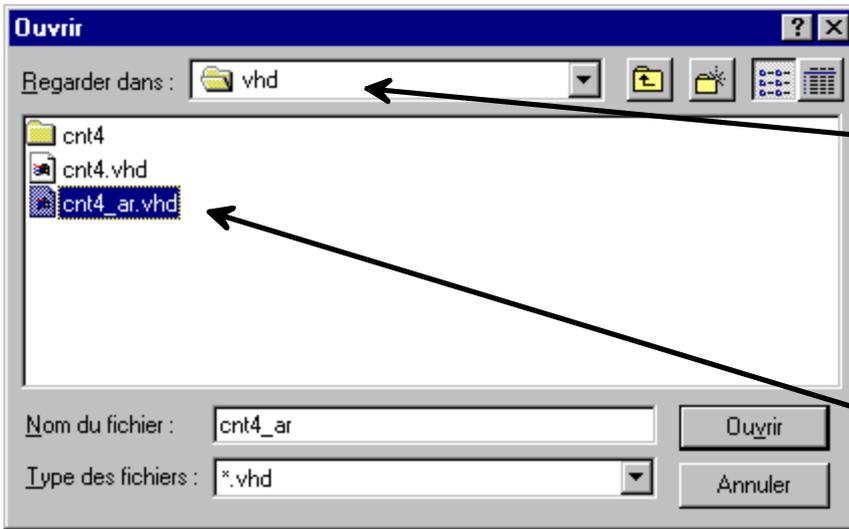


Appel de la simulation



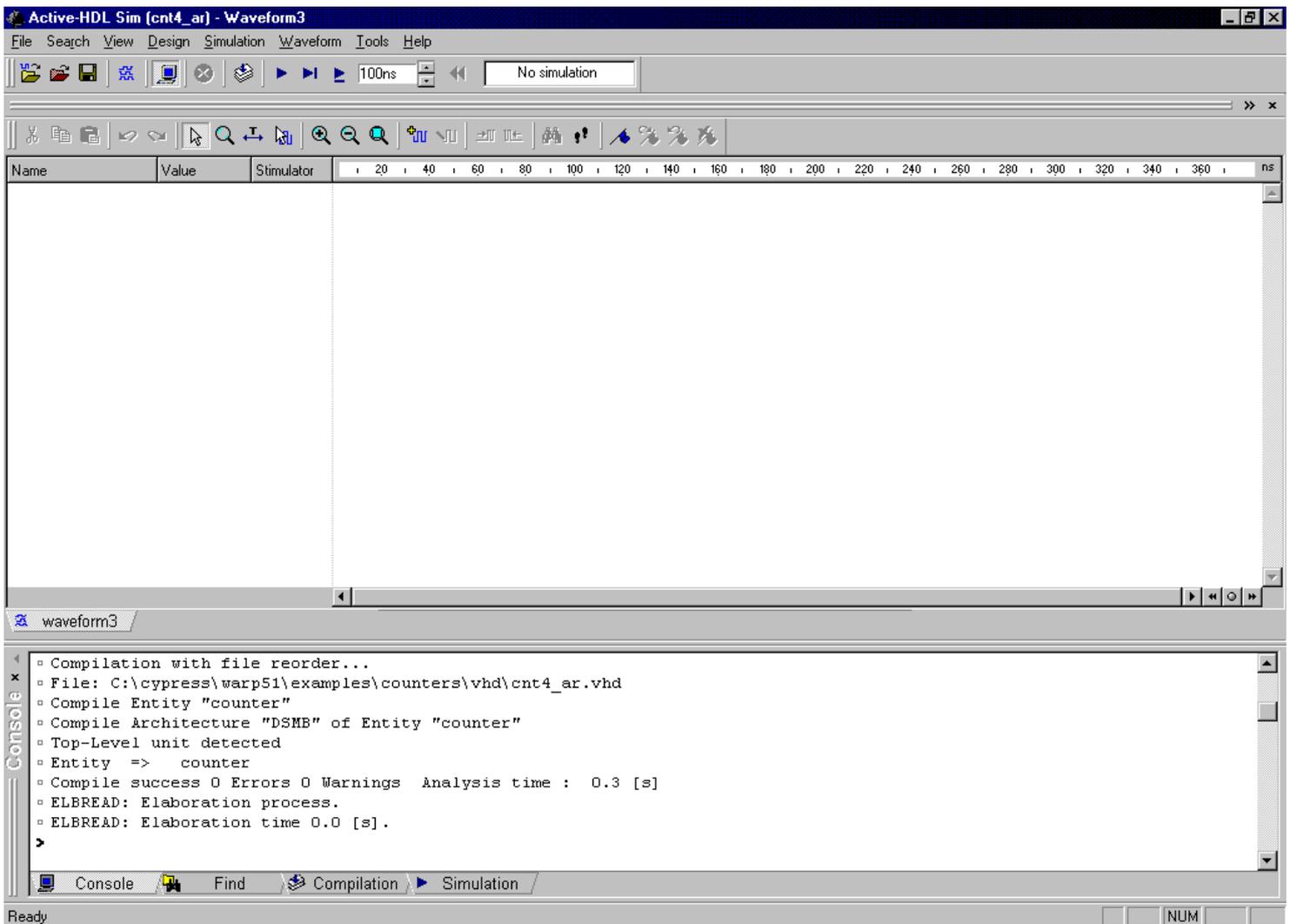
SYNTHESE VHDL AVEC GALAXY

SIMULATION DU PROJET CHARGEMENT DU FICHER A SIMULER



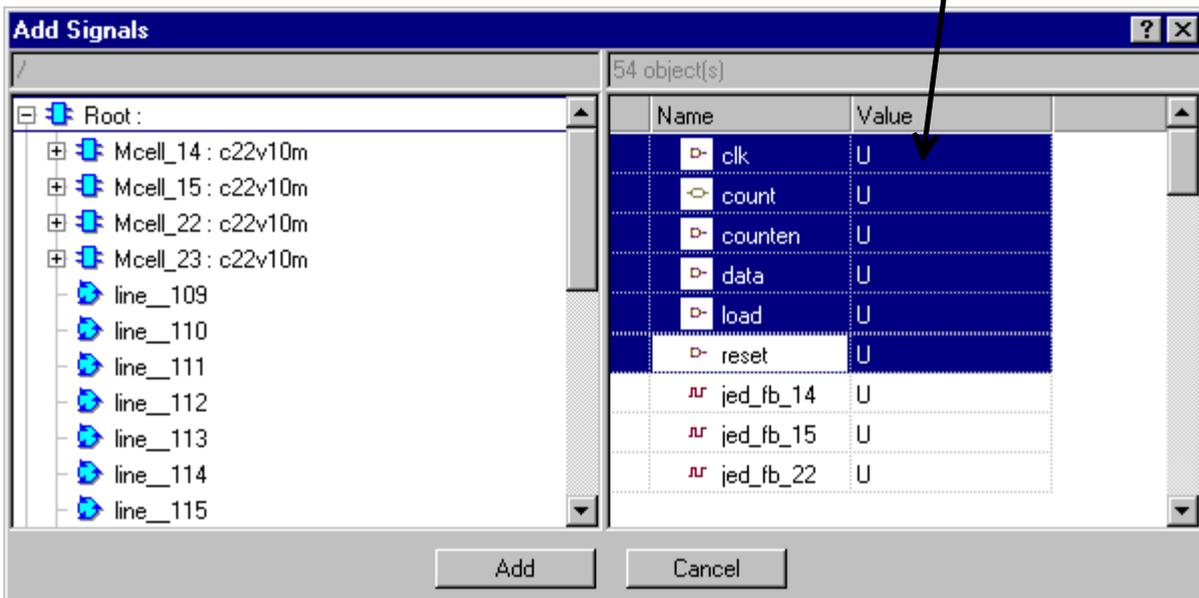
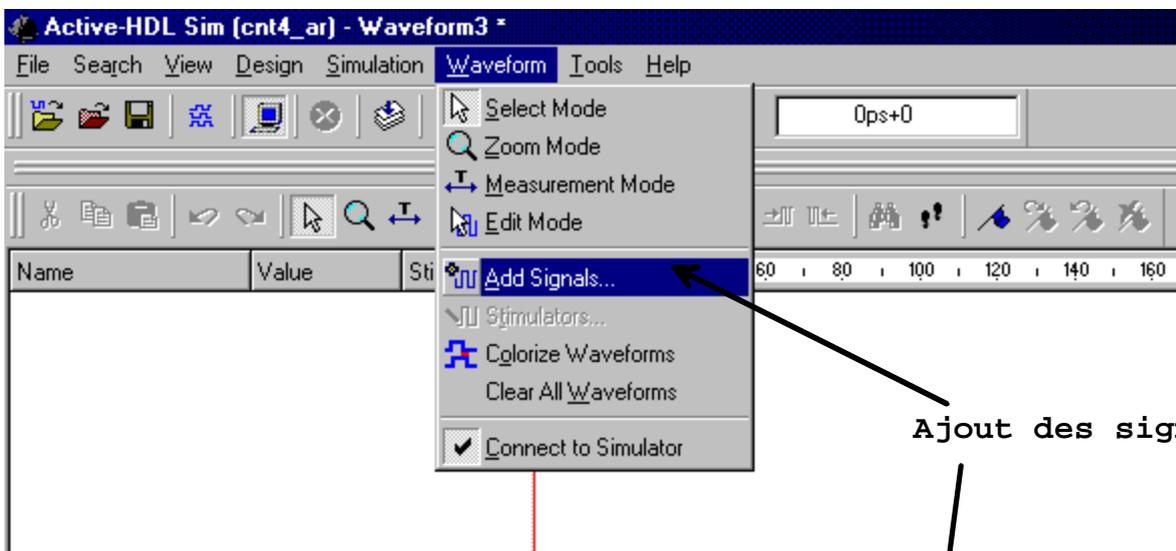
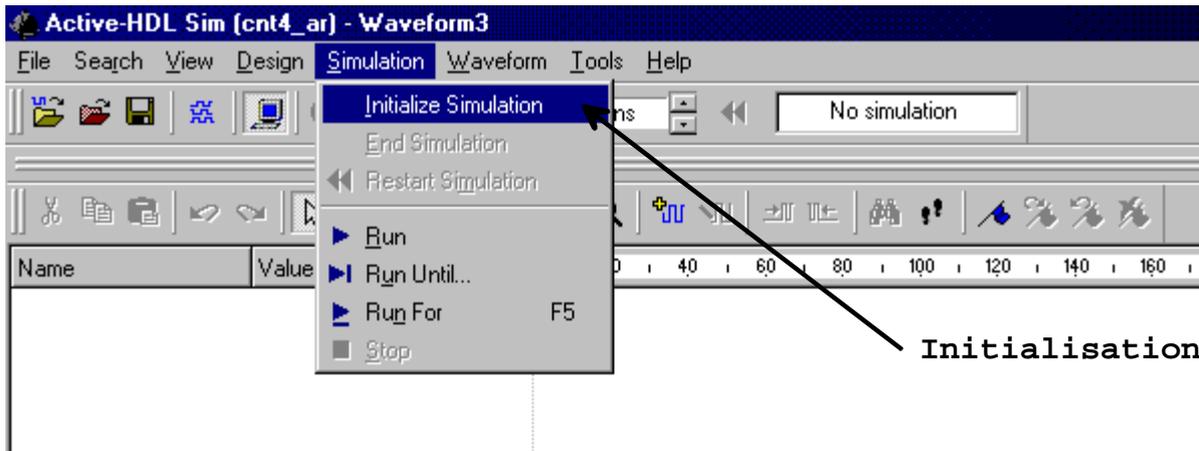
Sous répertoire \vhd\
contenant les fichiers
compilés avec l'option
post simulation du
compilateur

Chargement du fichier
à simulé



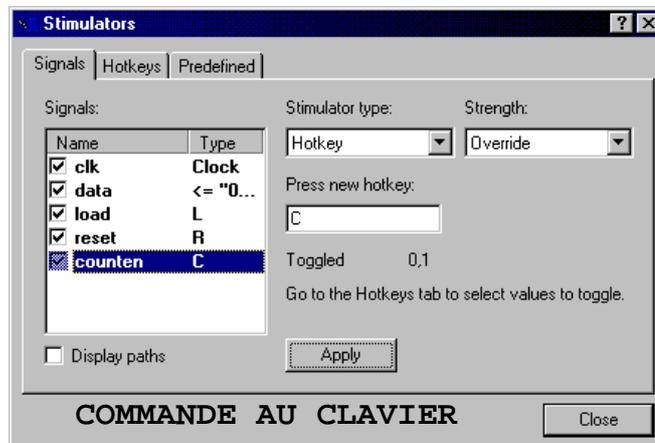
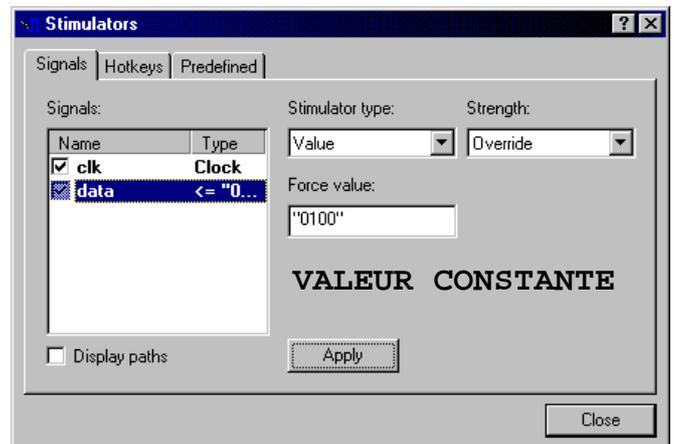
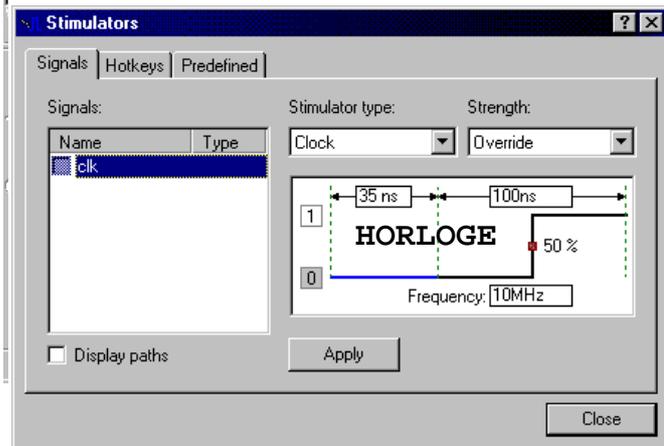
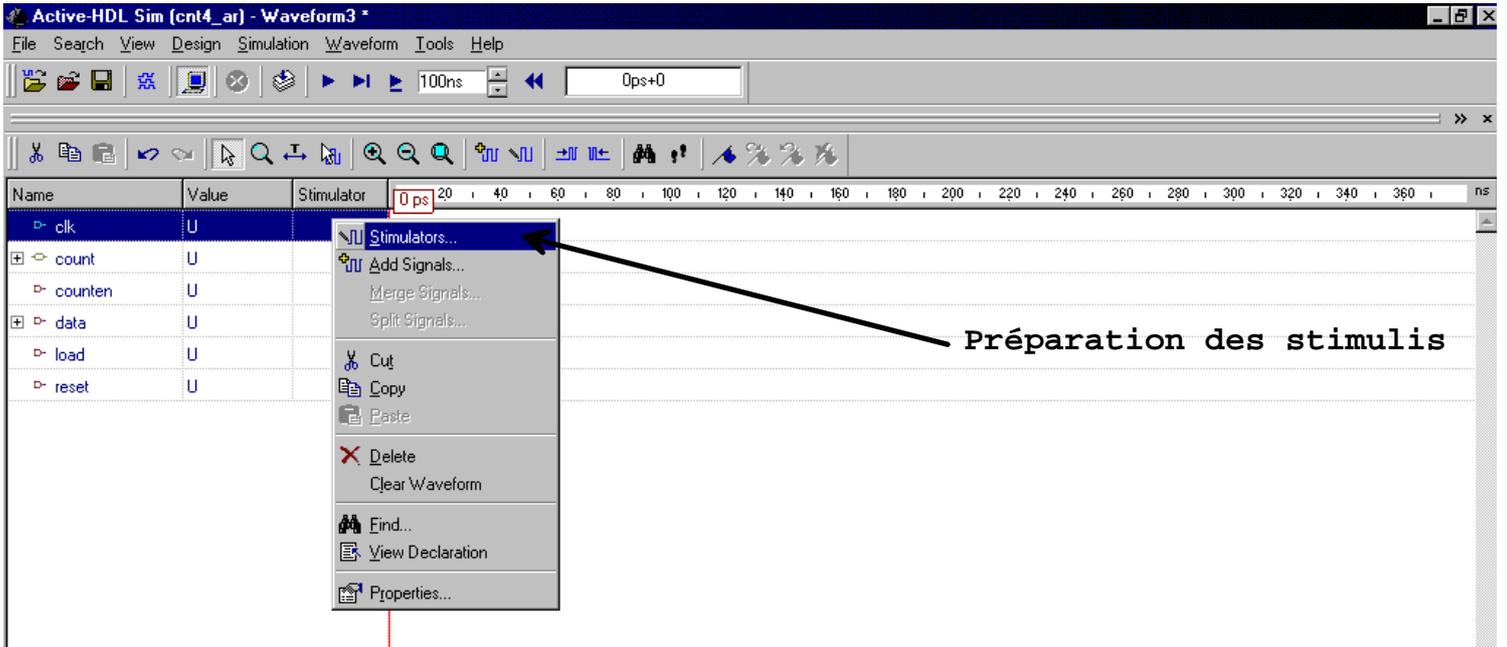
SYNTHESE VHDL AVEC GALAXY

PREPARATION DE LA SIMULATION CHOIX DES SIGNAUX



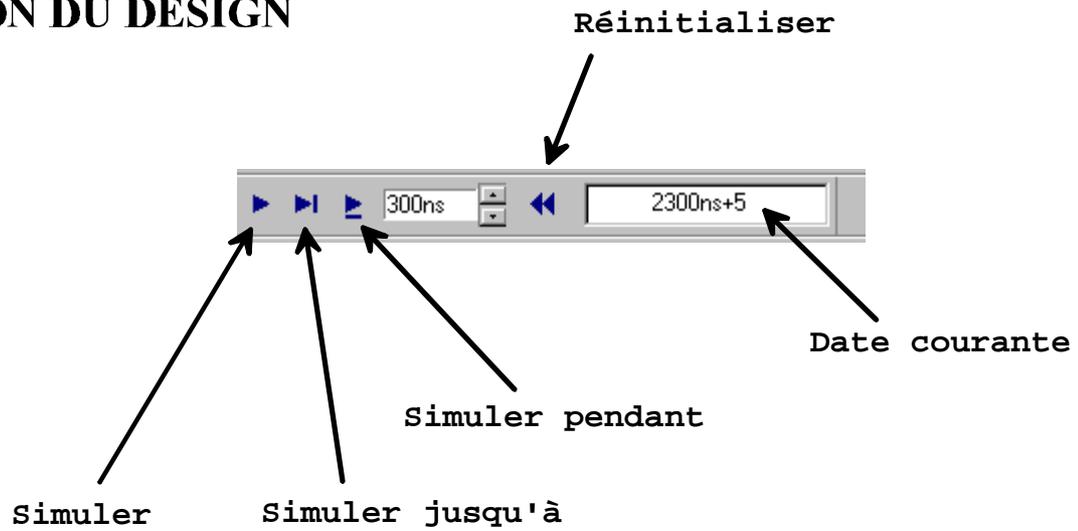
SYNTHESE VHDL AVEC GALAXY

PREPARATION DE LA SIMULATION EDITION DES STIMULIS



SYNTHESE VHDL AVEC GALAXY

SIMULATION DU DESIGN



The screenshot shows the Active-HDL Sim (cnt4_ar) - Waveform3 window. The top toolbar includes buttons for simulation control and a time field set to 300ns. The main area displays a waveform for signals: clk (Clock), count (hexadecimal values), counten (C), data (hexadecimal values), load (L), and reset (R). The console window at the bottom shows the following output:

```
▫ KERNEL: stopped at time: 100 ns
▫ KERNEL: stopped at time: 200 ns
▫ KERNEL: stopped at time: 500 ns
▫ KERNEL: stopped at time: 800 ns
▫ KERNEL: stopped at time: 1100 ns
▫ KERNEL: stopped at time: 1400 ns
▫ KERNEL: stopped at time: 1700 ns
▫ KERNEL: stopped at time: 2 us
▫ KERNEL: stopped at time: 2300 ns
```