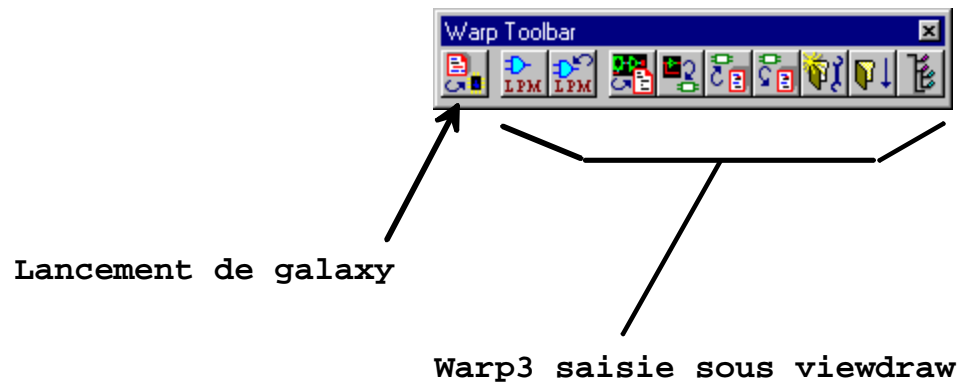
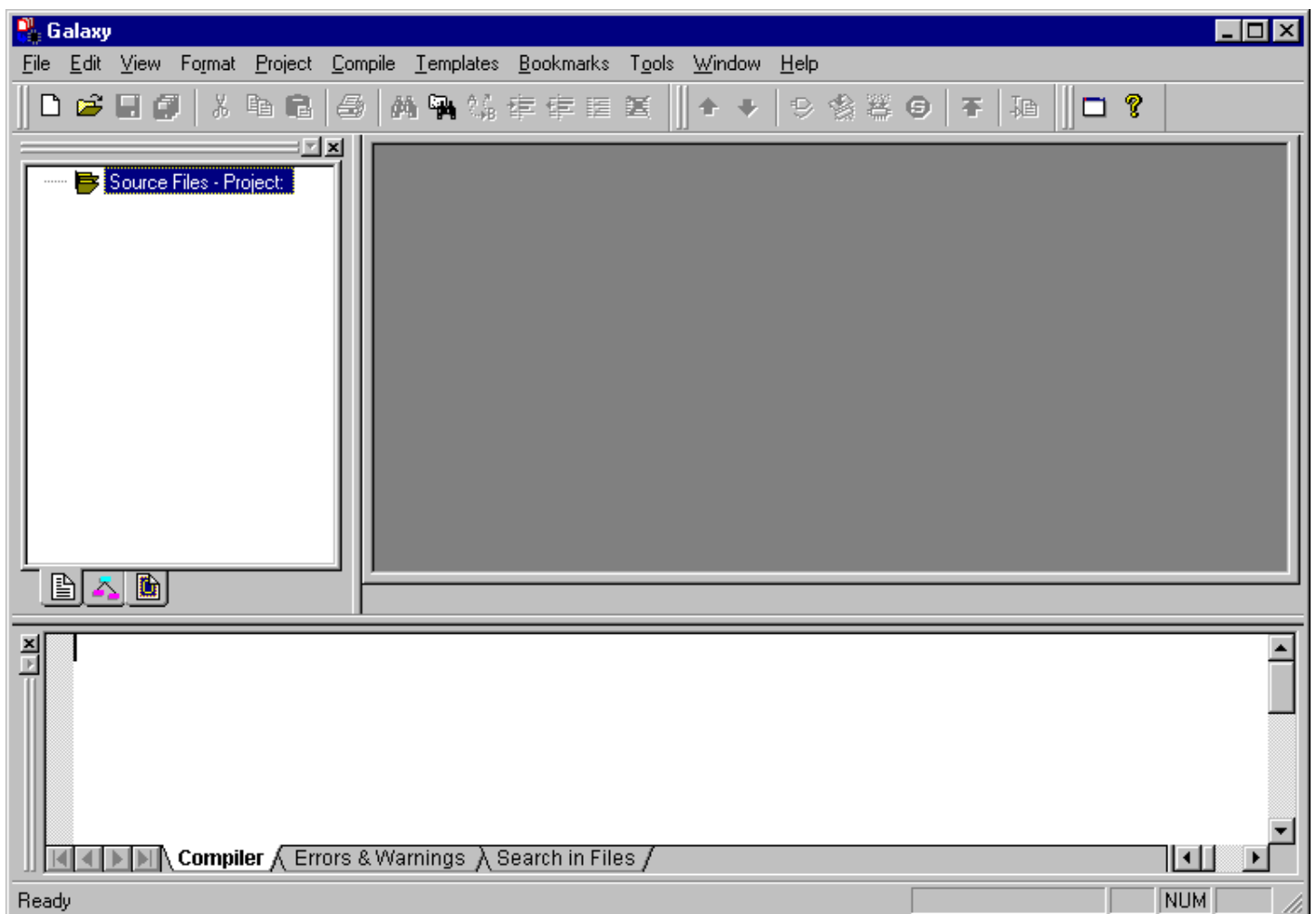


SYNTHESE VHDL AVEC GALAXY

LA BARRE D'OUTILS DE GALAXY



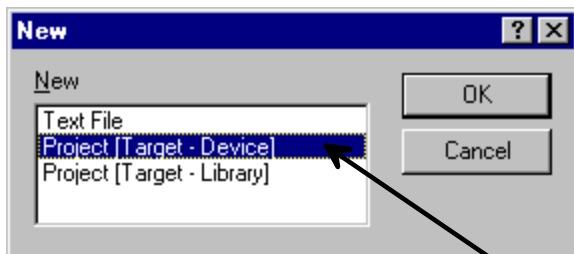
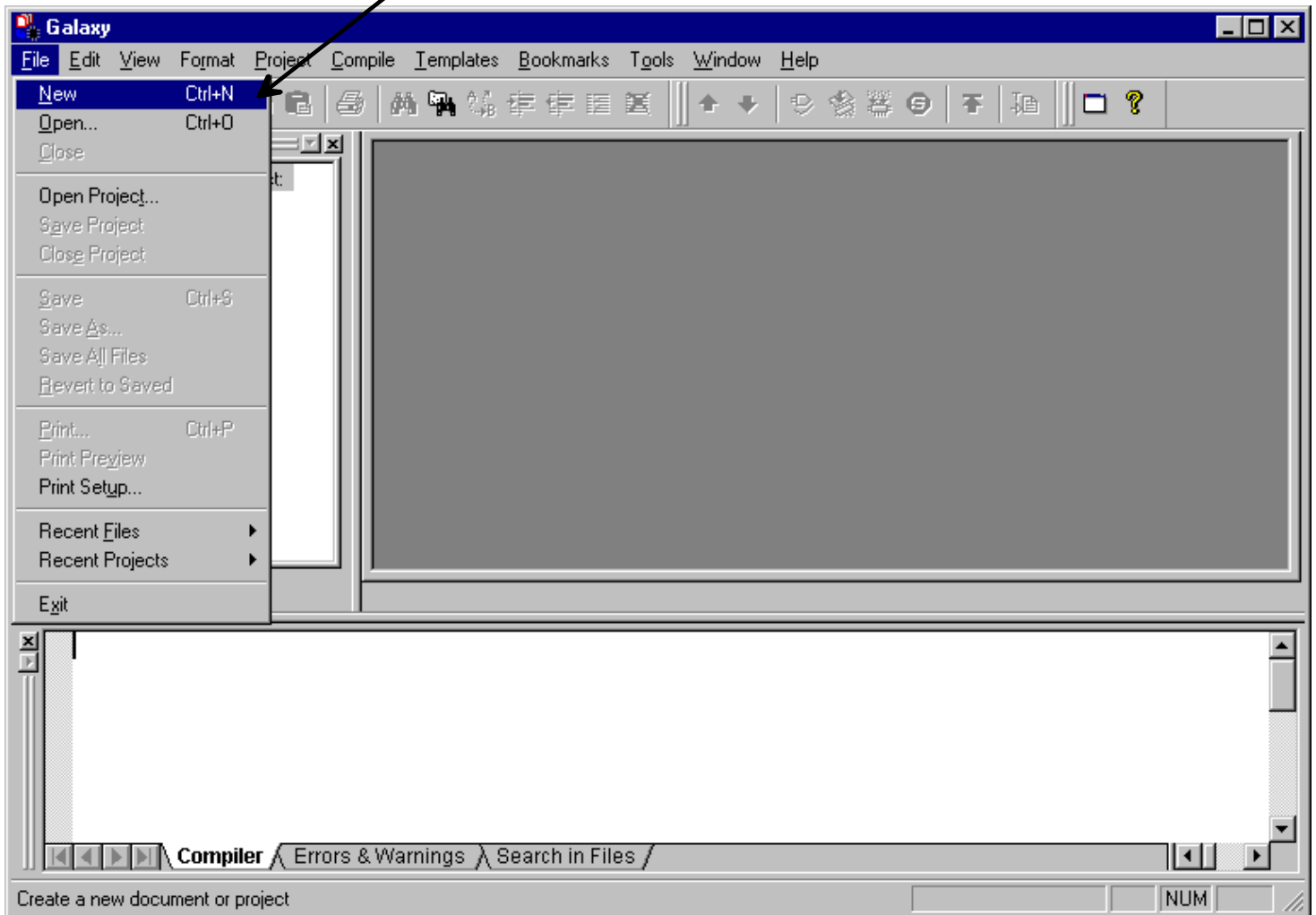
DEMARRAGE DE GALAXY



SYNTHESE VHDL AVEC GALAXY

RECUPERATION D'UN FICHIER EXISTANT

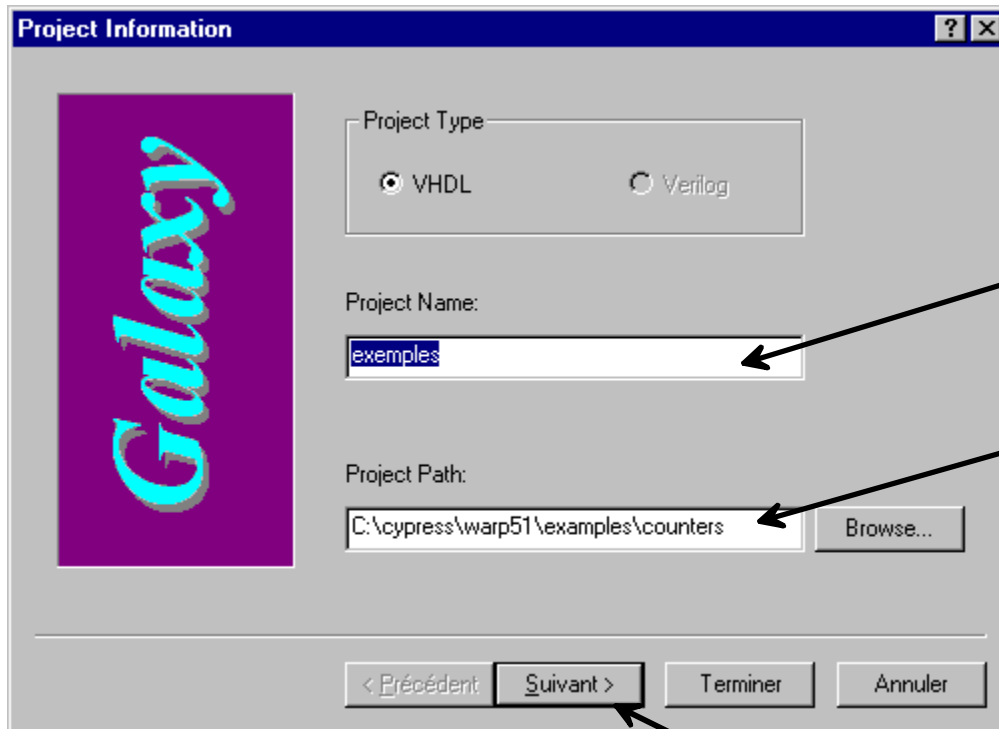
Faire Fichier Nouveau



Choisir Nouveau projet

SYNTHESE VHDL AVEC GALAXY

RECUPERATION DE FICHIERS EXISTANTS

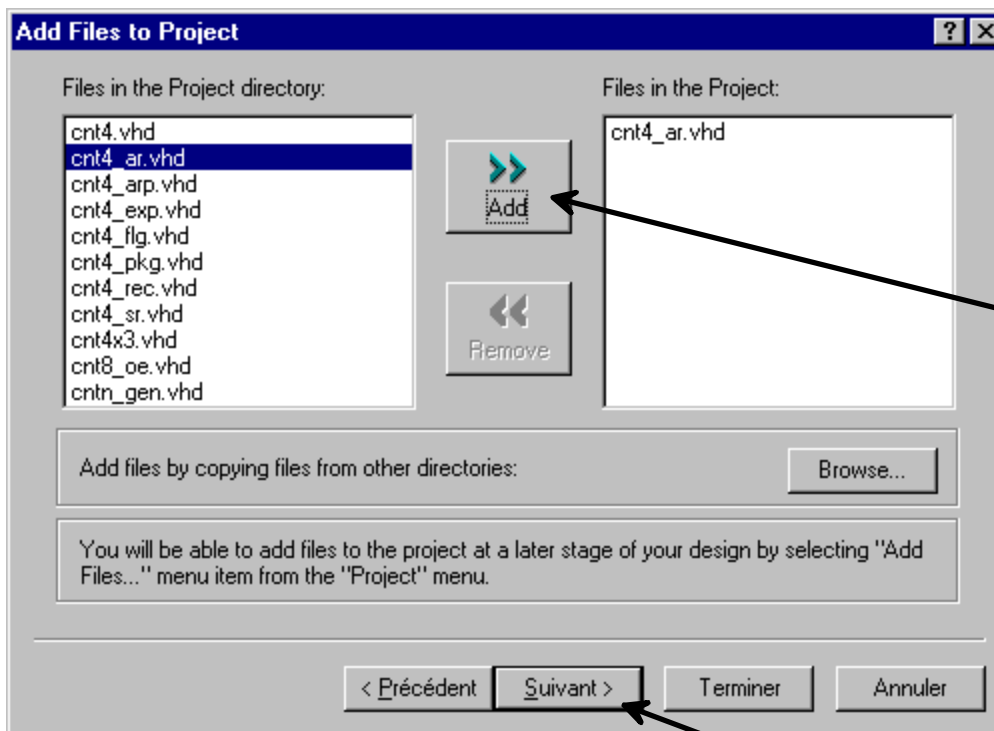


The 'Project Information' dialog box features a blue title bar with a question mark and close button. On the left is a purple rectangle with the word 'Galaxy' in a stylized, light blue font. The main area contains two radio buttons for 'Project Type': 'VHDL' (selected) and 'Verilog'. Below is a 'Project Name' text field containing 'exemples'. Further down is a 'Project Path' text field containing 'C:\cypress\warp51\examples\counters', followed by a 'Browse...' button. At the bottom are four buttons: '< Précédent', 'Suivant >', 'Terminer', and 'Annuler'.

Donner le nom
du projet

Donner le répertoire
du projet

Pour continuer



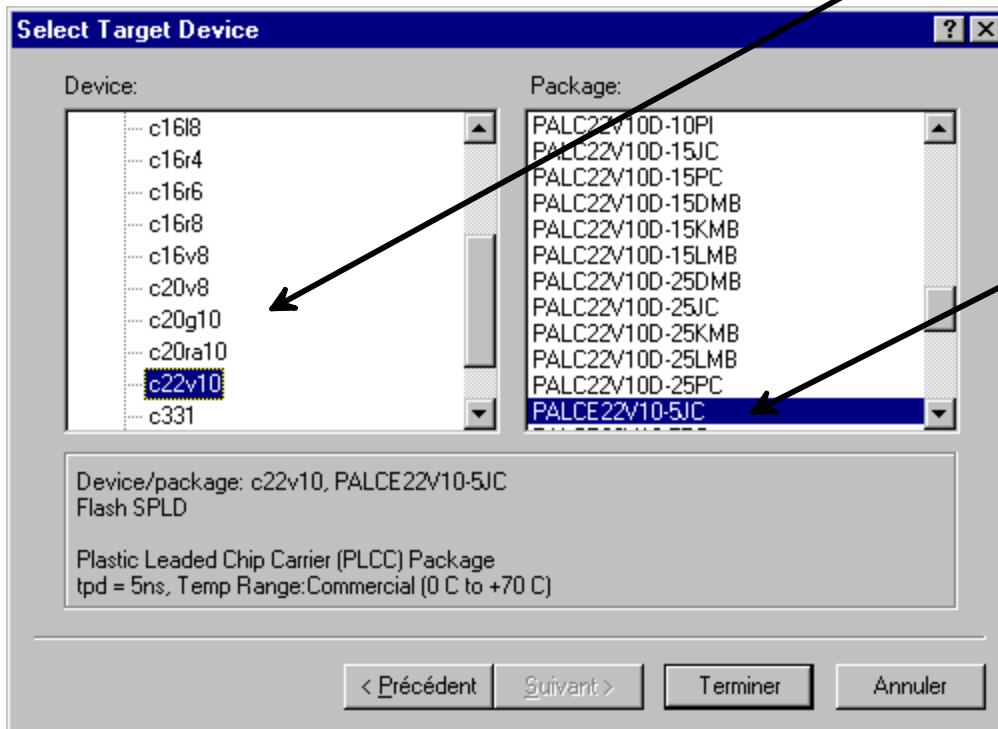
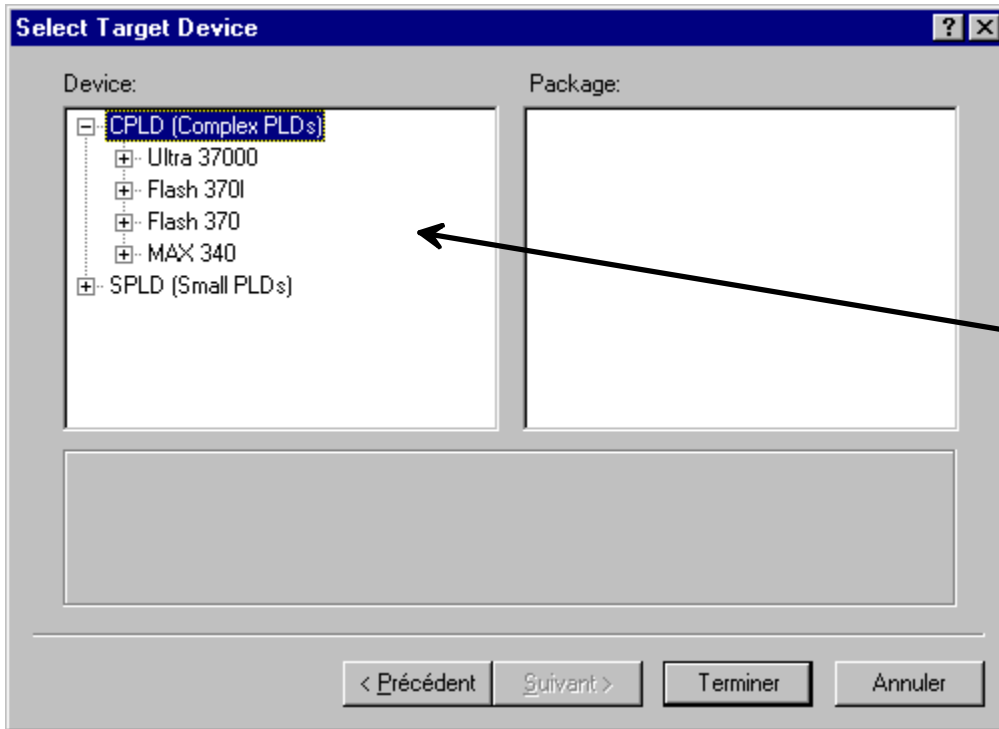
The 'Add Files to Project' dialog box has a blue title bar with a question mark and close button. It is divided into two panes. The left pane, 'Files in the Project directory:', lists several .vhd files, with 'cnt4_ar.vhd' selected. The right pane, 'Files in the Project:', shows 'cnt4_ar.vhd'. Between the panes are 'Add' and 'Remove' buttons. Below the panes is a section for 'Add files by copying files from other directories:' with a 'Browse...' button. A text box at the bottom explains that files can be added later via the 'Add Files...' menu item. At the bottom are four buttons: '< Précédent', 'Suivant >', 'Terminer', and 'Annuler'.

Ajout des fichiers
vhd1 faisant partie
du projet

Pour continuer

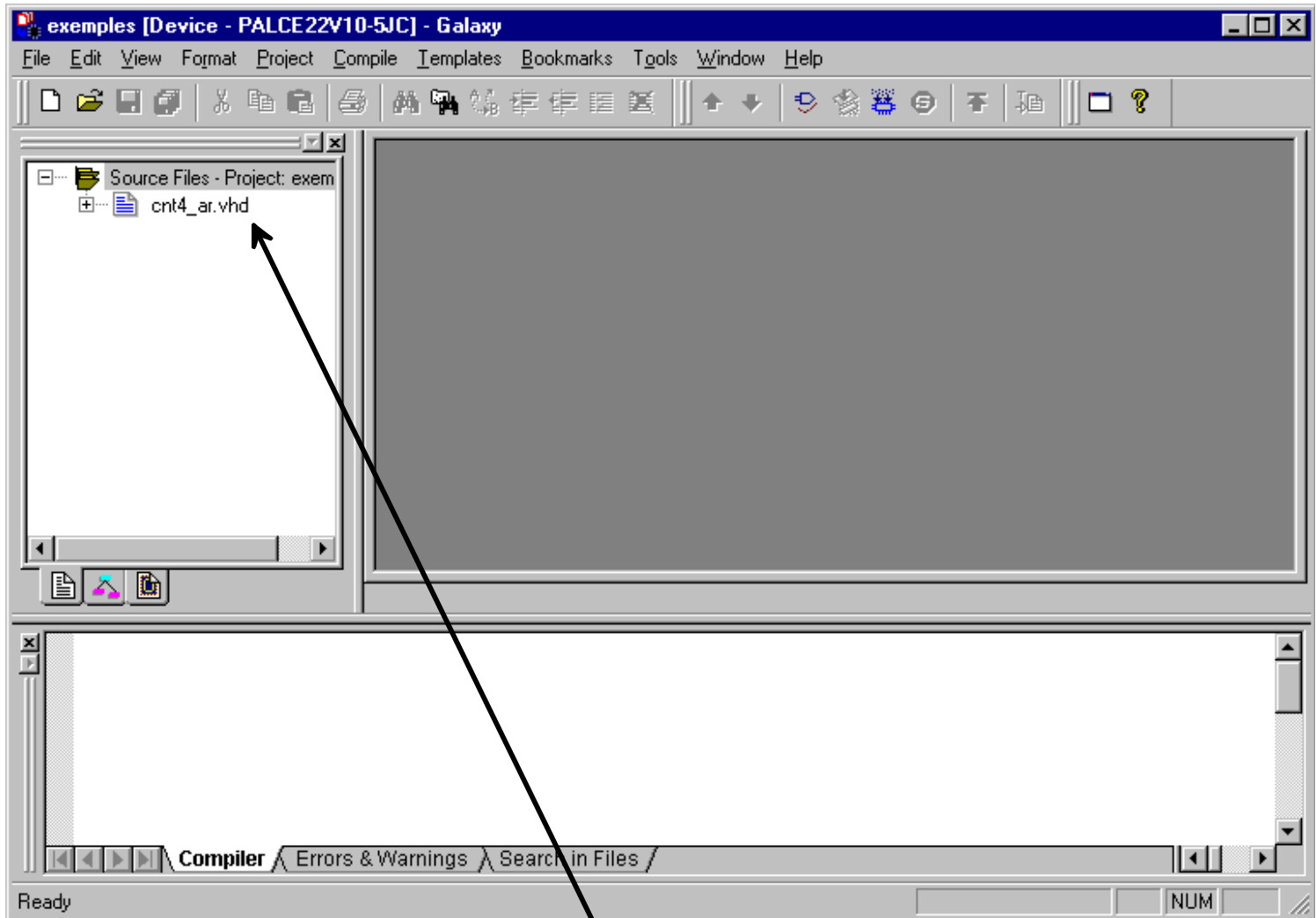
SYNTHESE VHDL AVEC GALAXY

CHOIX DU COMPOSANT CIBLE (DEVICE)



SYNTHESE VHDL AVEC GALAXY

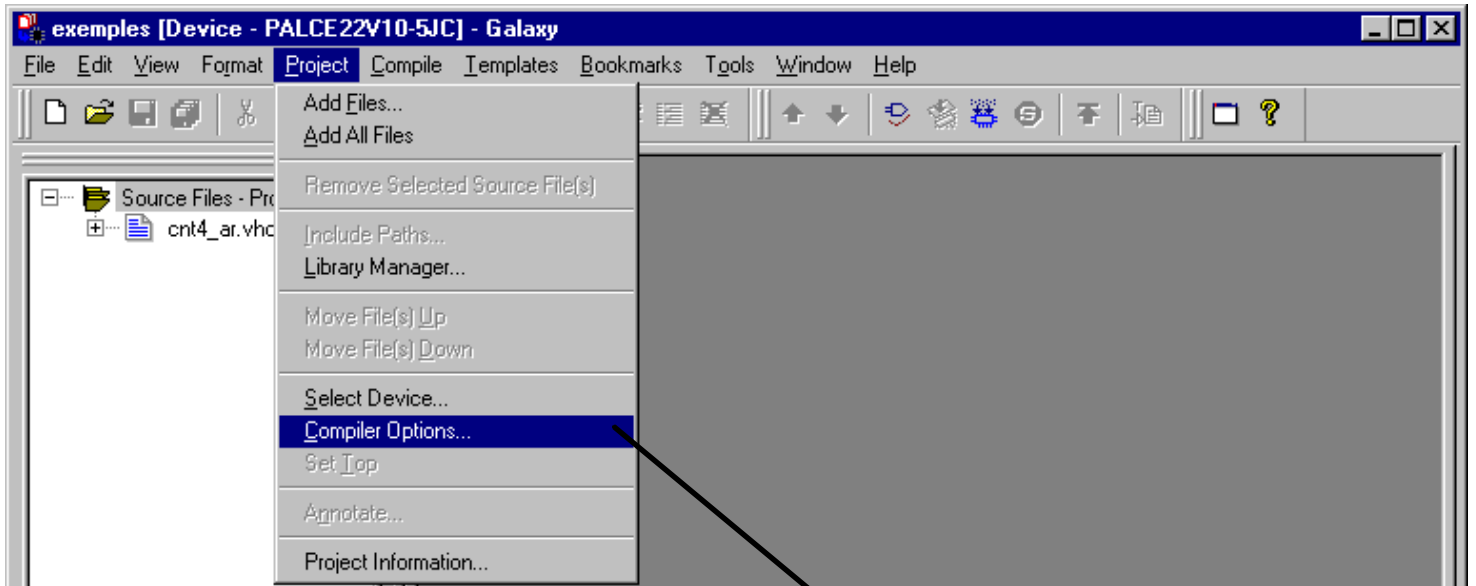
OUVERTURE DE GALAXY



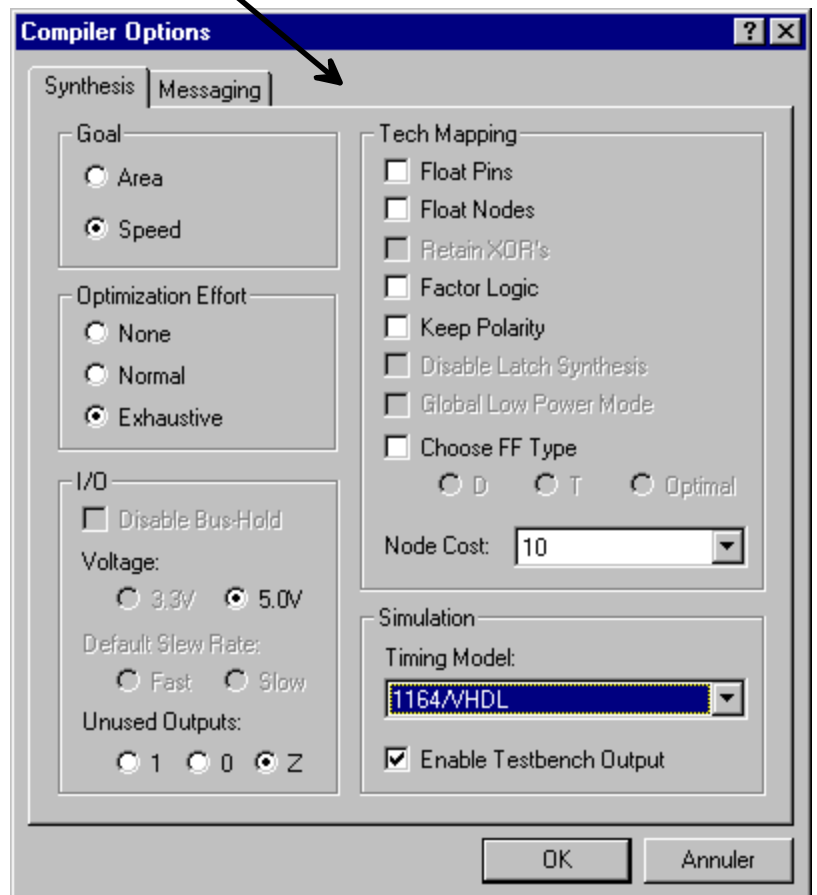
Listes des fichiers
vhd1 présent dans le projet

SYNTHESE VHDL AVEC GALAXY

PREPARATION A LA COMPILATION

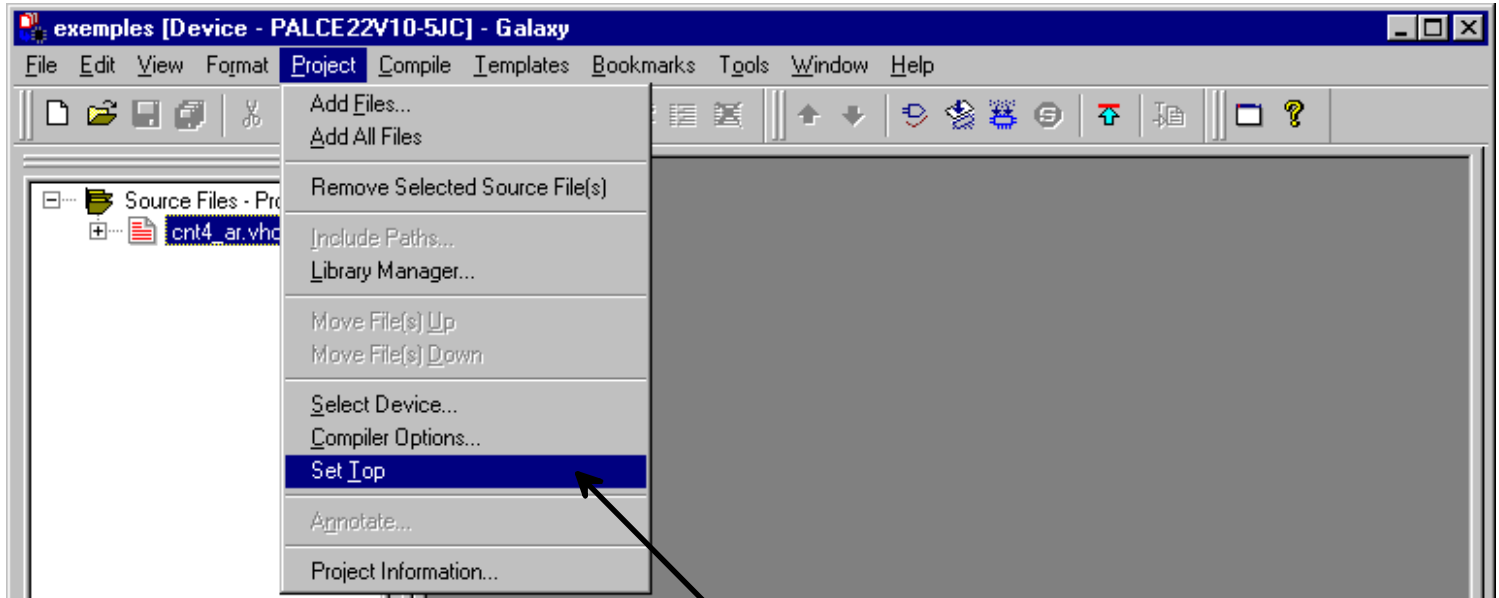


Saisie des options de compilation



SYNTHESE VHDL AVEC GALAXY

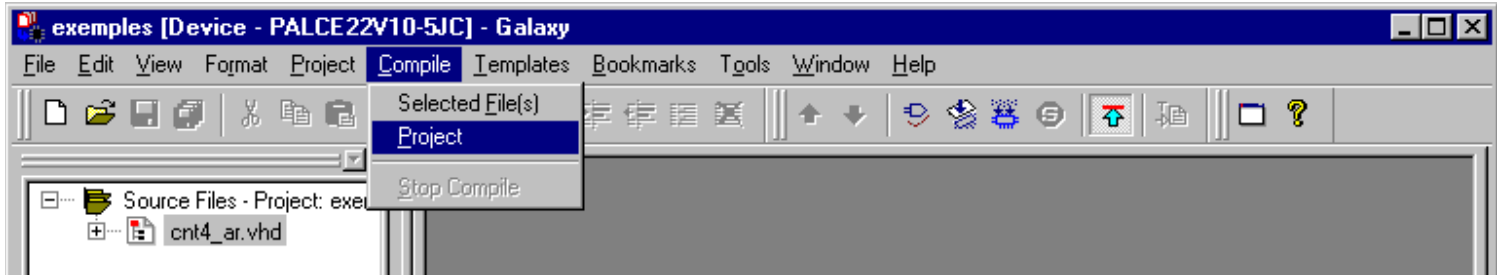
DESIGNATION DE L'ORIGINE DE LA DESCRIPTION VHDL OPTION SET TOP



Désignation du fichier
contenant le point de
départ de la description vhd1

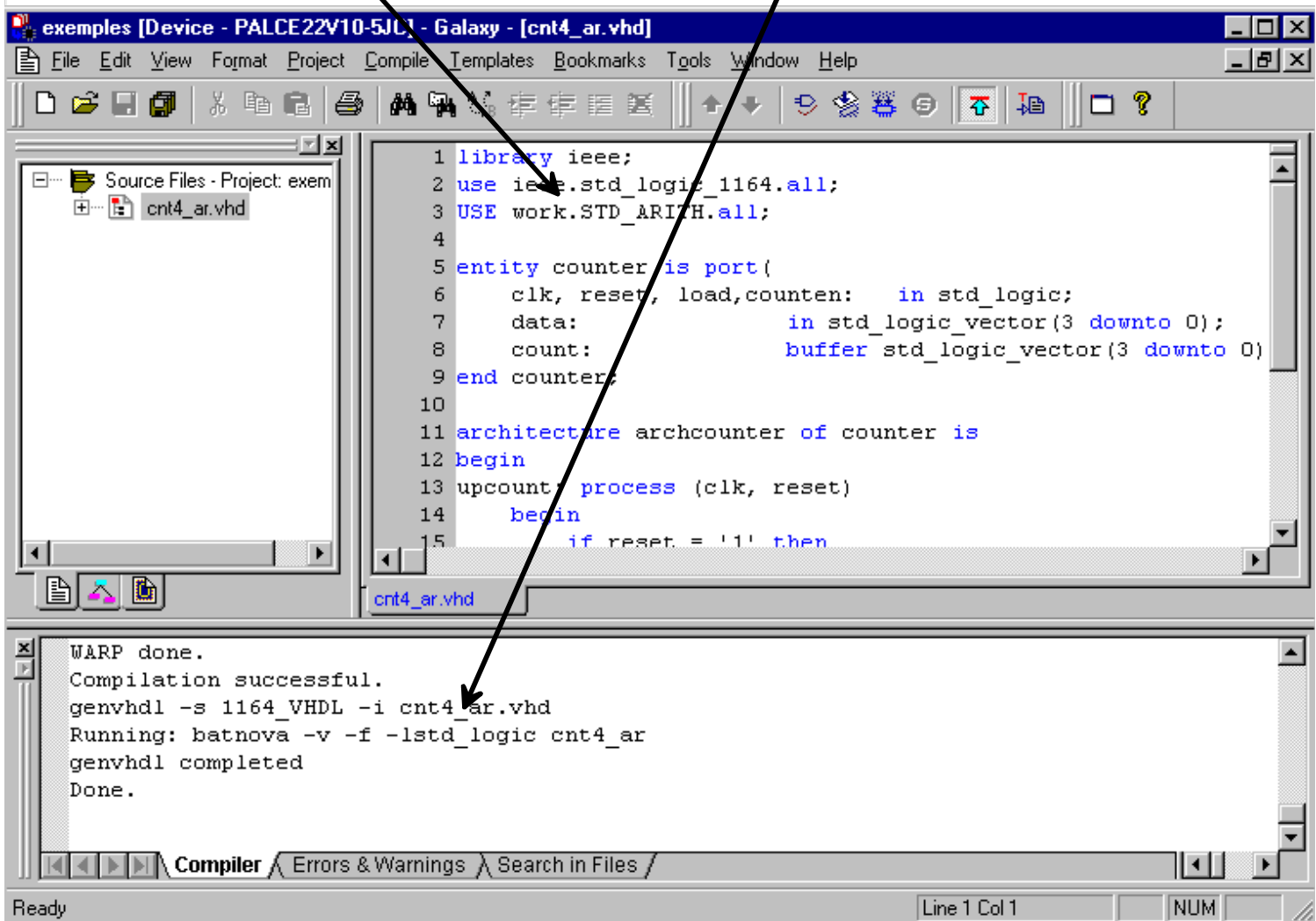
SYNTHESE VHDL AVEC GALAXY

COMPILATION DU PROJET



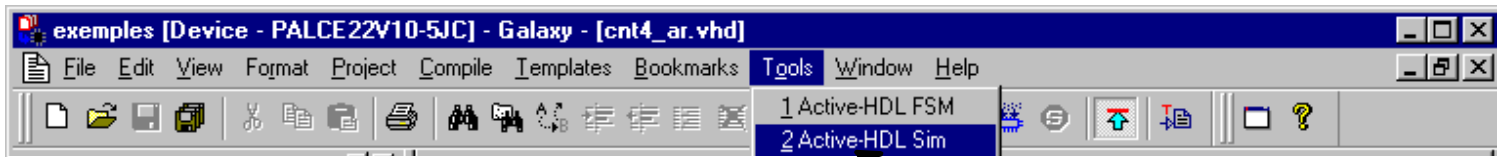
Texte vhdl

Résultat de la compilation

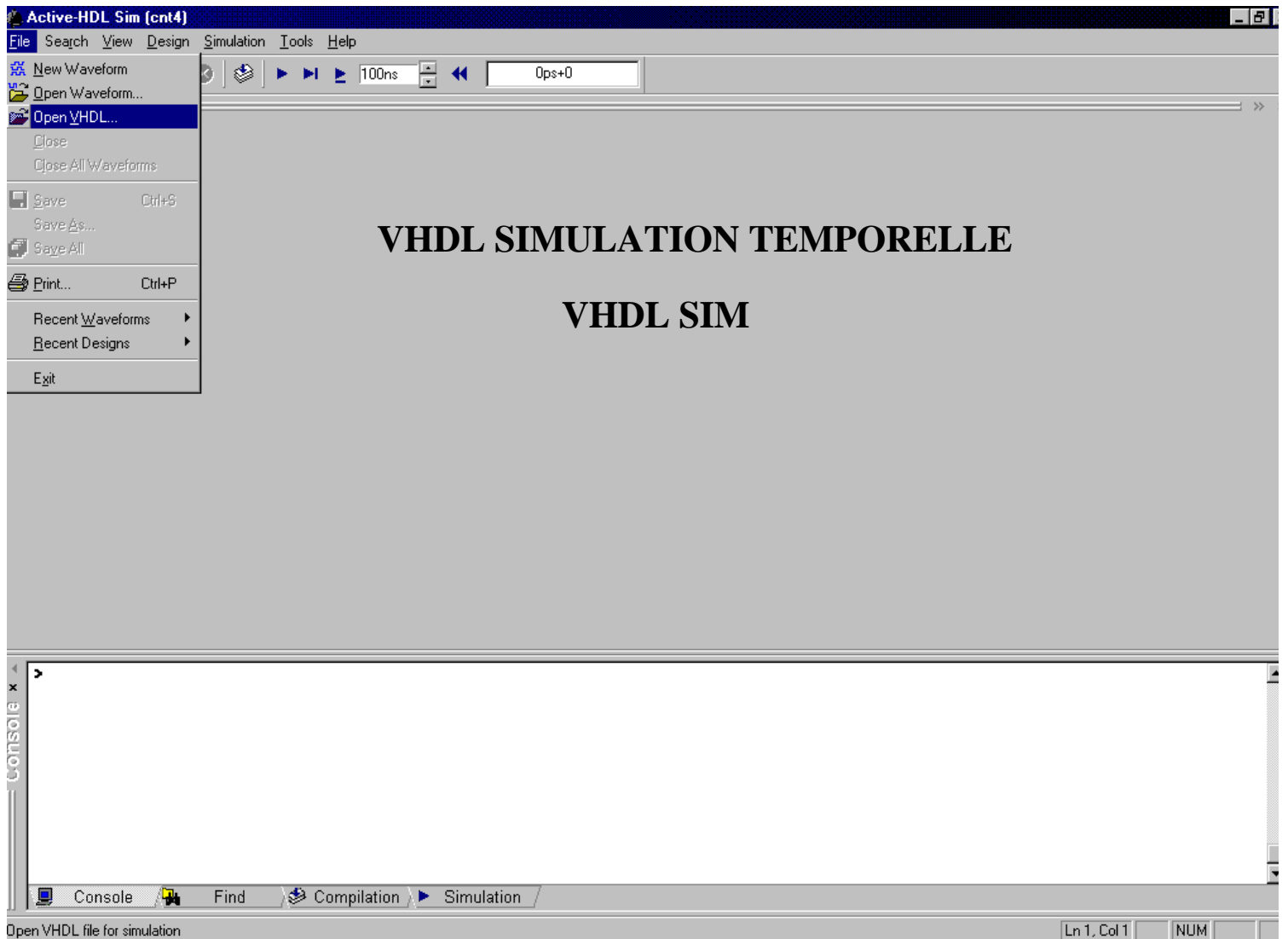


SYNTHESE VHDL AVEC GALAXY

SIMULATION DU PROJET

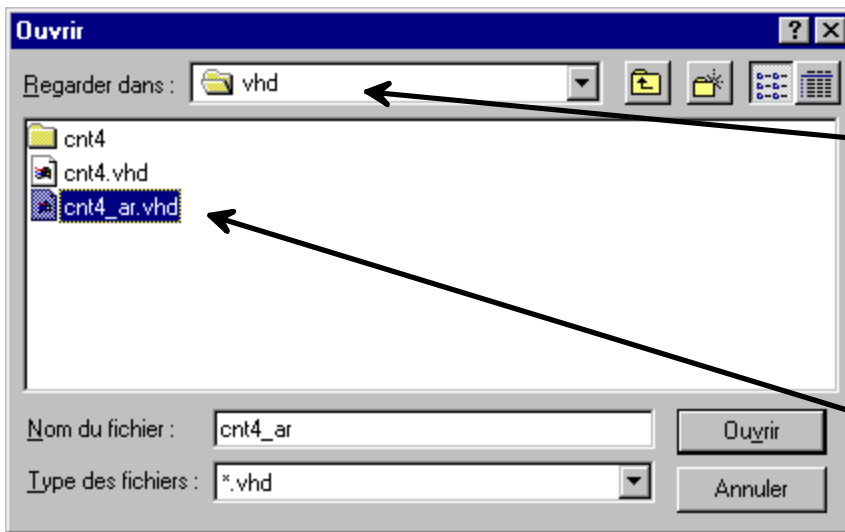


Appel de la simulation



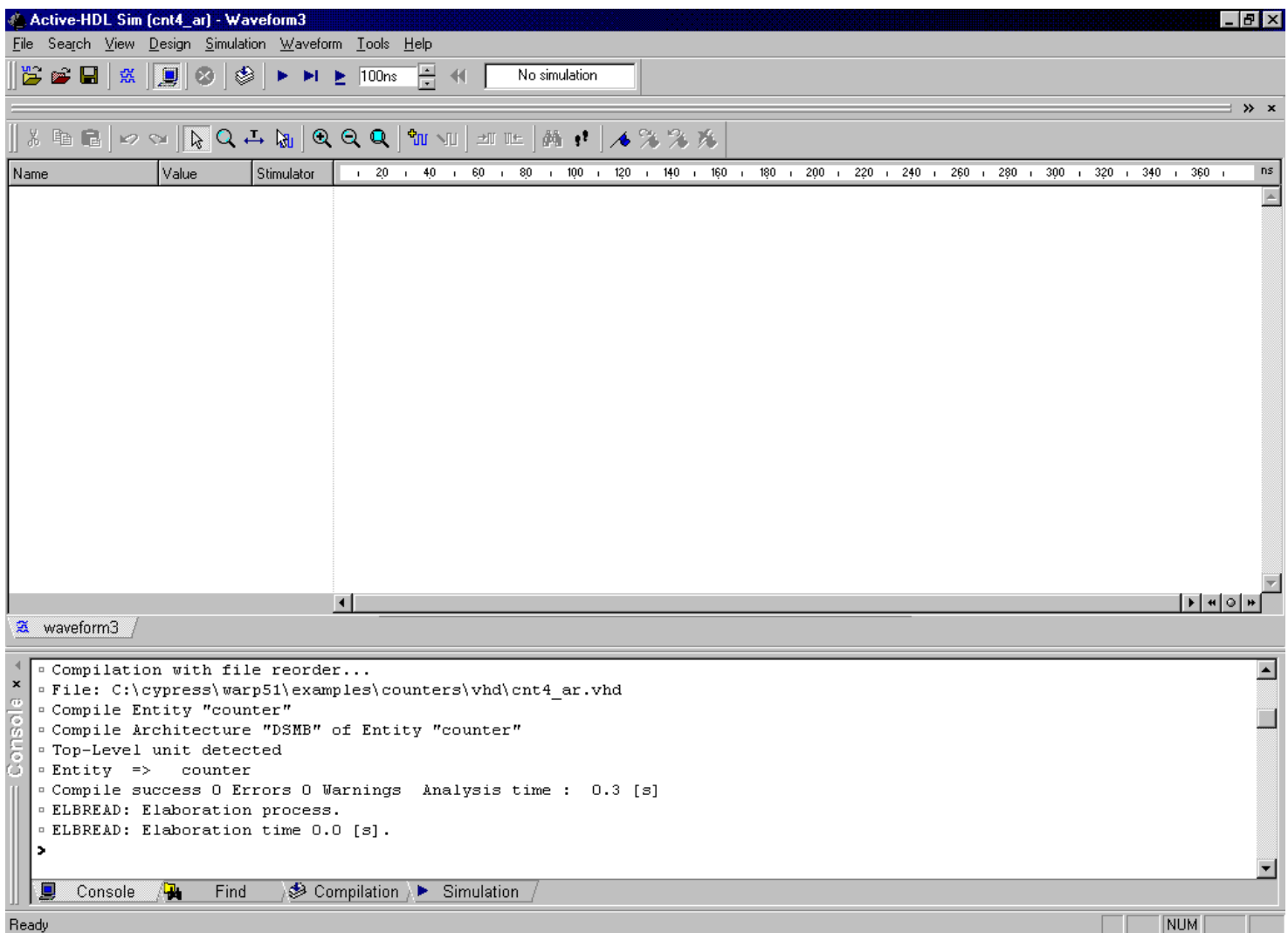
SYNTHESE VHDL AVEC GALAXY

SIMULATION DU PROJET CHARGEMENT DU FICHIER A SIMULER



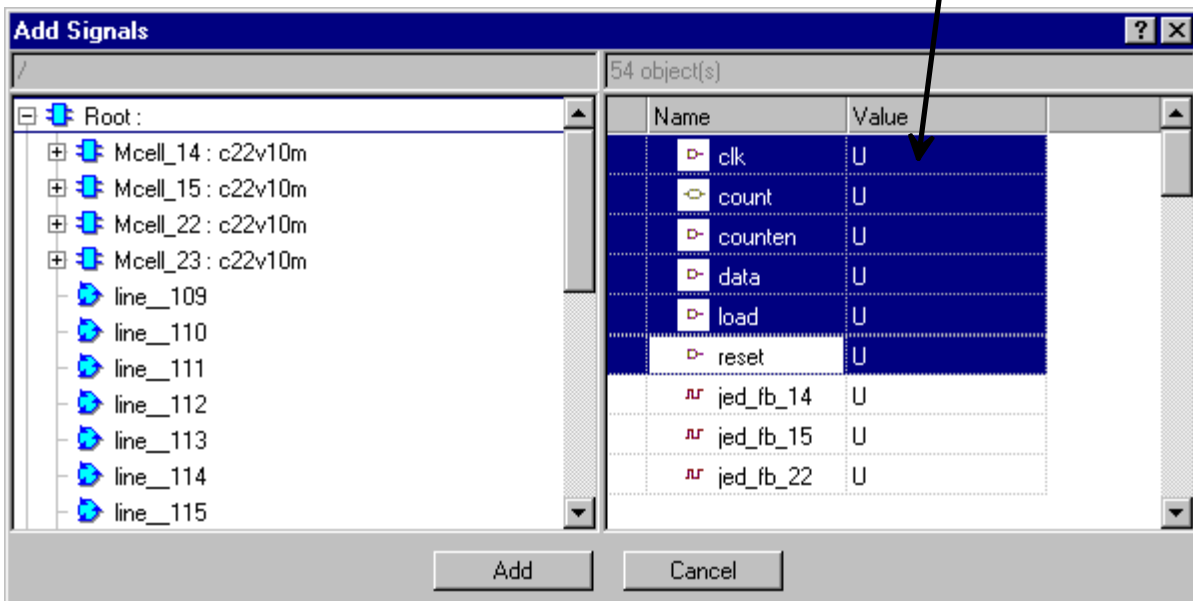
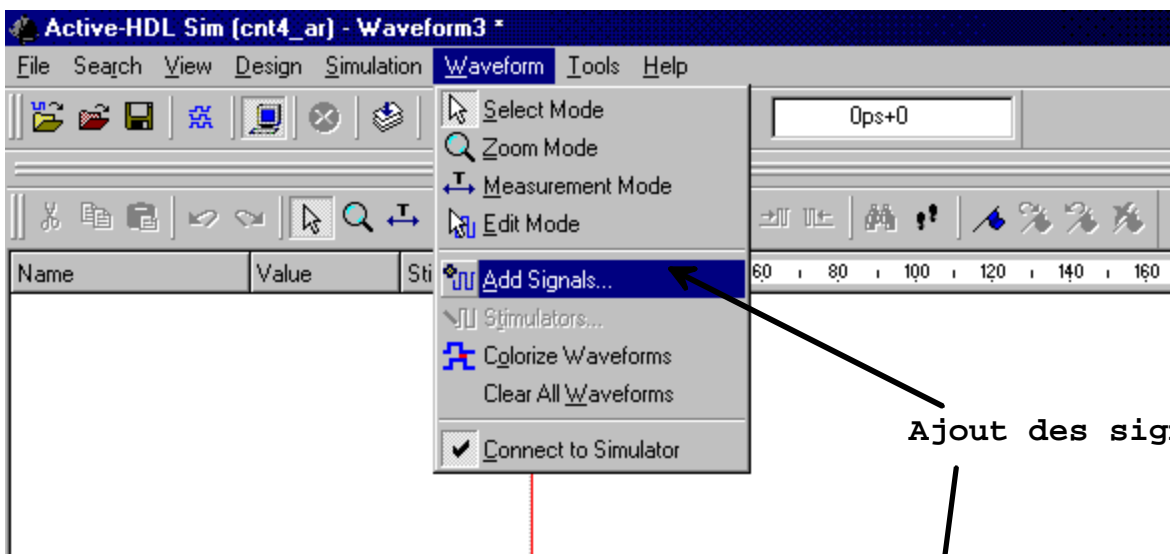
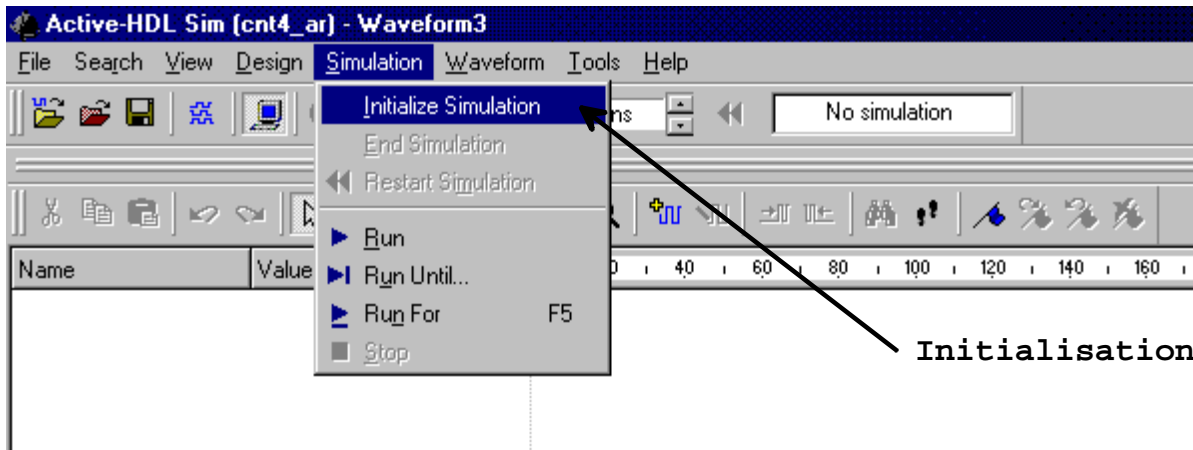
Sous répertoire \vhd\
contenant les fichiers
compilés avec l'option
post simulation du
compilateur

Chargement du fichier
à simuler



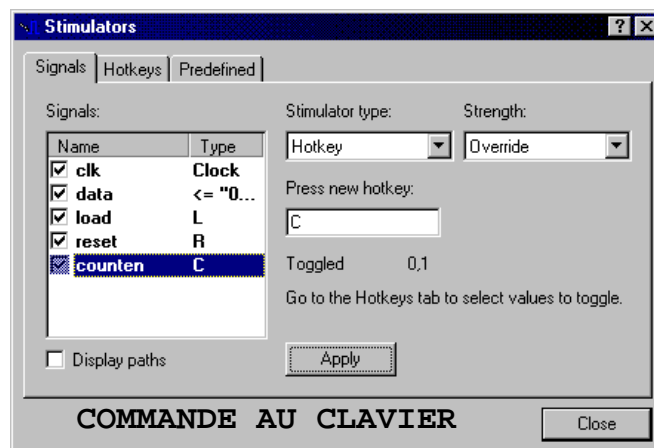
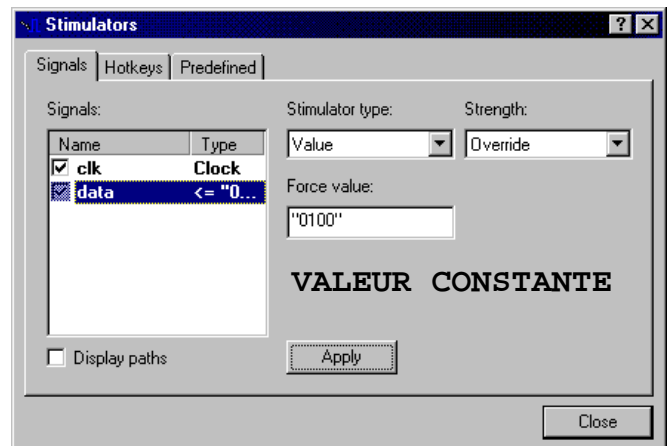
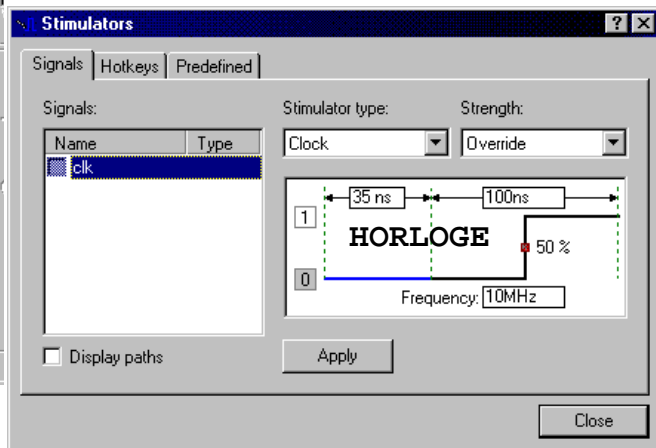
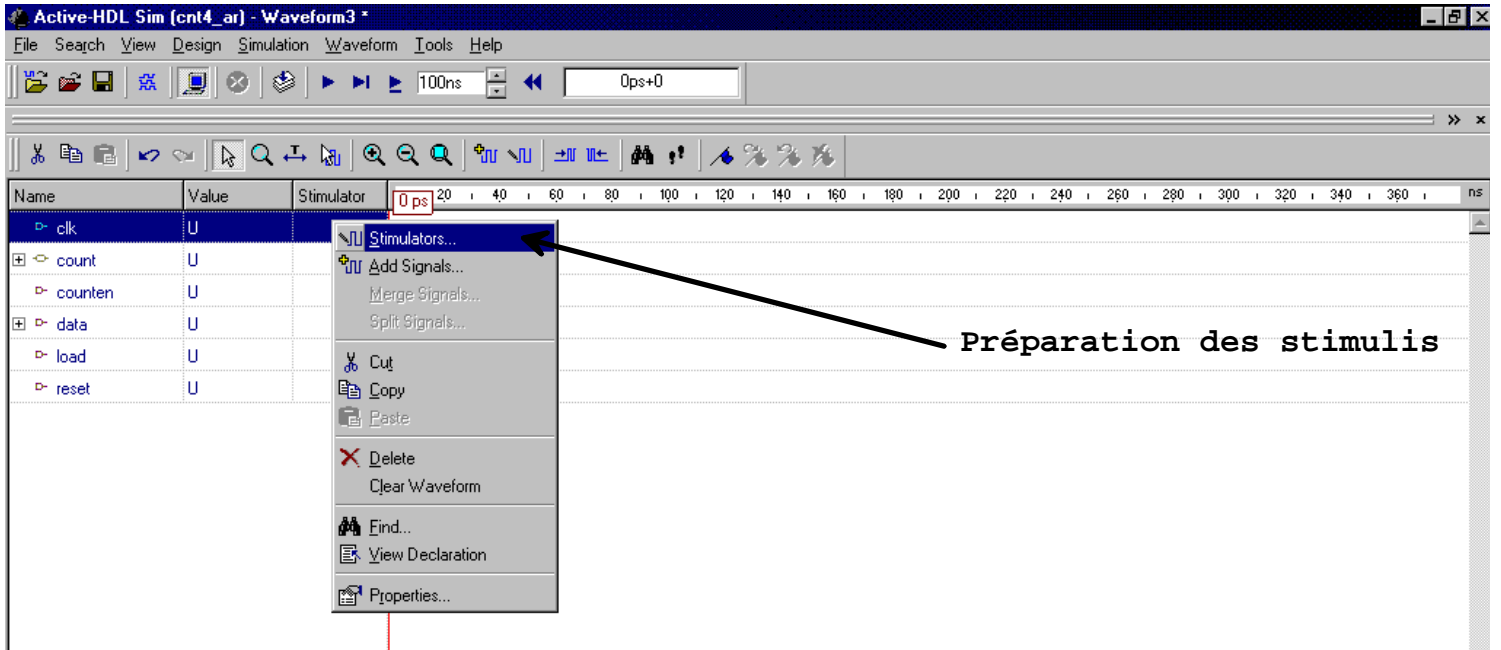
SYNTHESE VHDL AVEC GALAXY

PREPARATION DE LA SIMULATION CHOIX DES SIGNAUX



SYNTHESE VHDL AVEC GALAXY

PREPARATION DE LA SIMULATION EDITION DES STIMULIS



SYNTHESE VHDL AVEC GALAXY

SIMULATION DU DESIGN

